



C661-416L-CD

AIR TRAINING COMMAND

COMPUTER SYSTEM DEPARTMENT

AN/FSQ-7 INPUT/OUTPUT SYSTEM SCHEMATICS

CIRCUIT DIAGRAMS

Course Nr. ABR30533-1

KEESLER AFB, MISS

FOR ATC INSTRUCTIONAL PURPOSES ONLY

SCHEMATICS

FOR

INPUT/OUTPUT

SYSTEMS

AN/FSQ-7

COMBAT DIRECTION CENTRAL

TRAINING MANUAL

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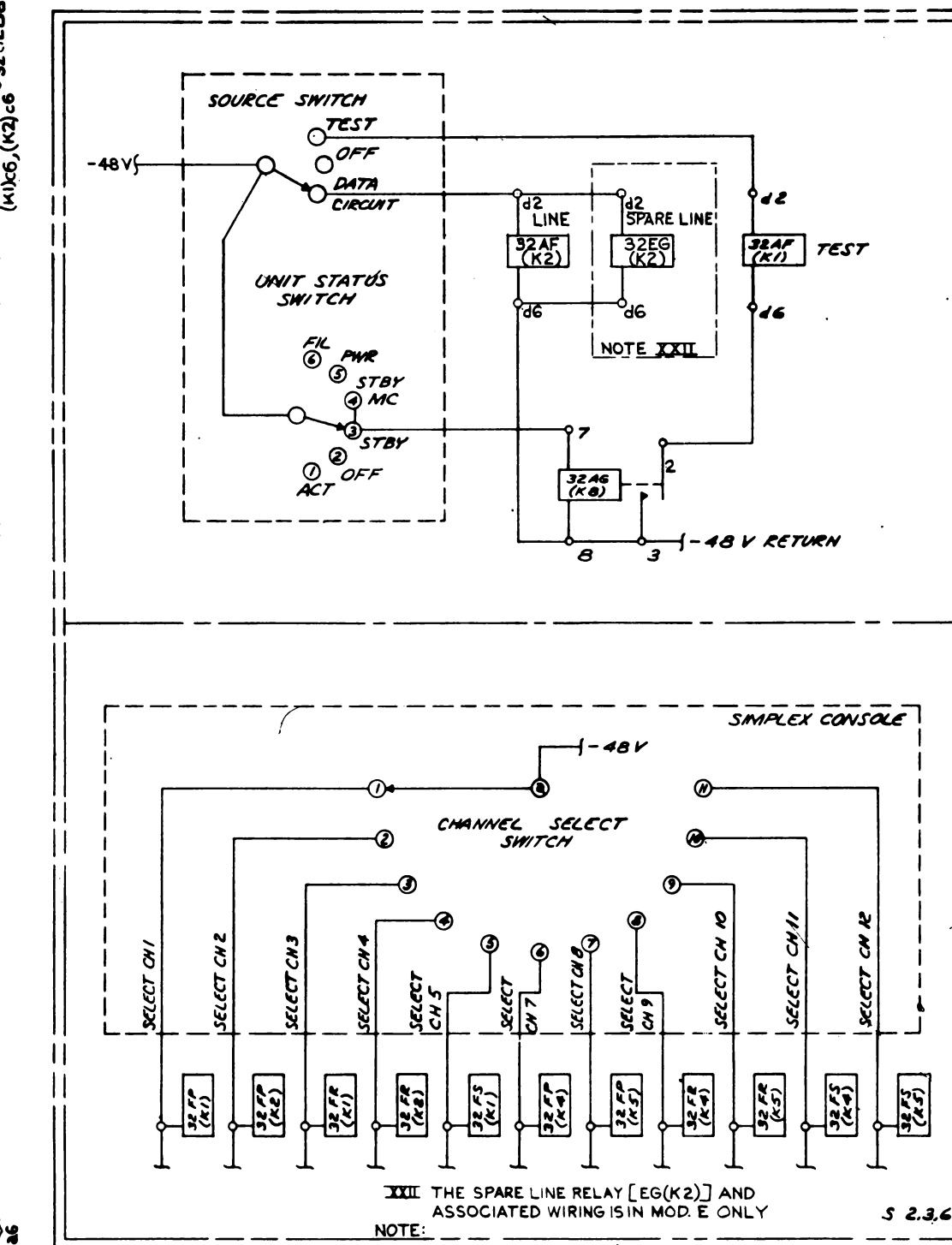
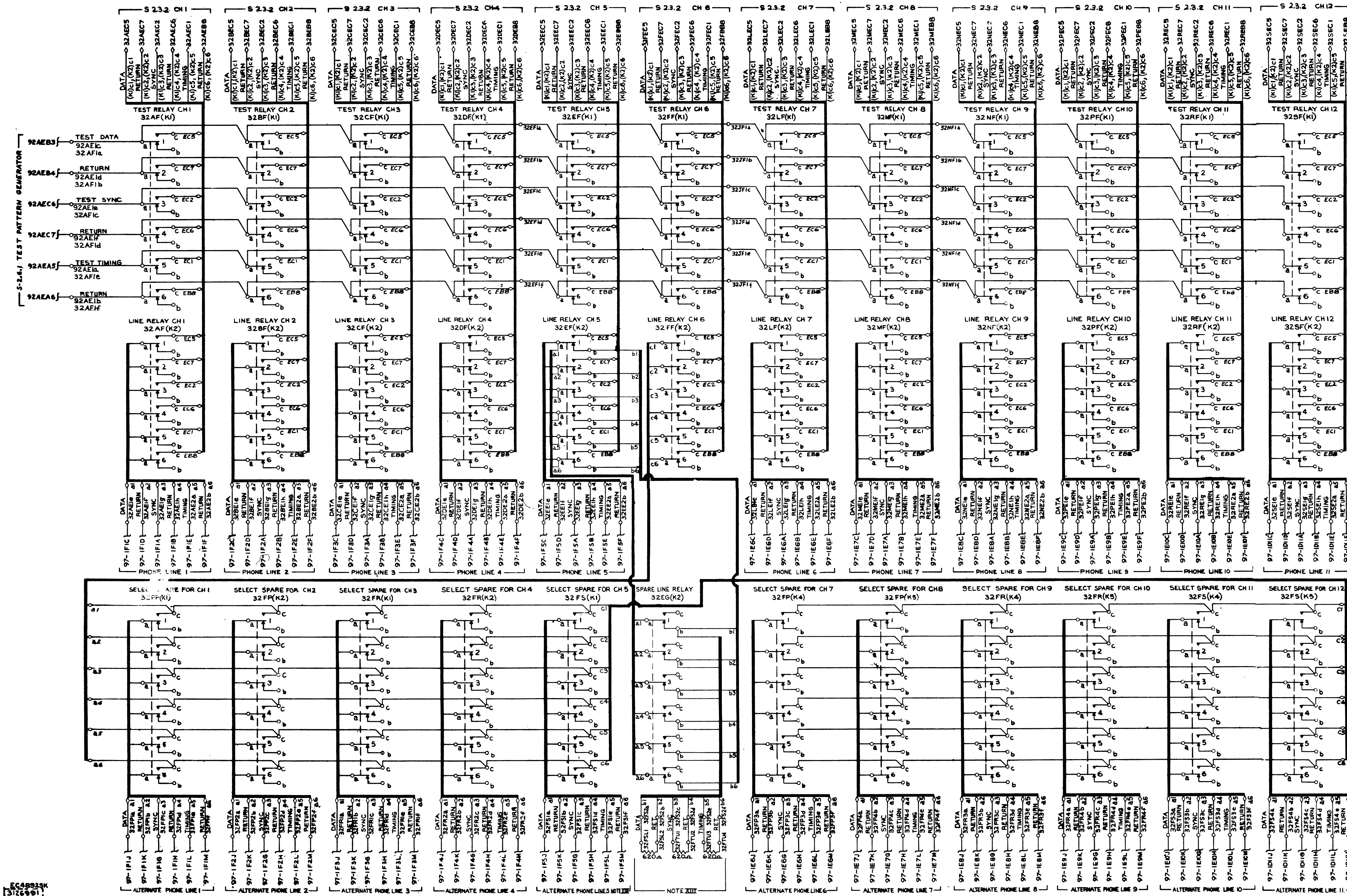
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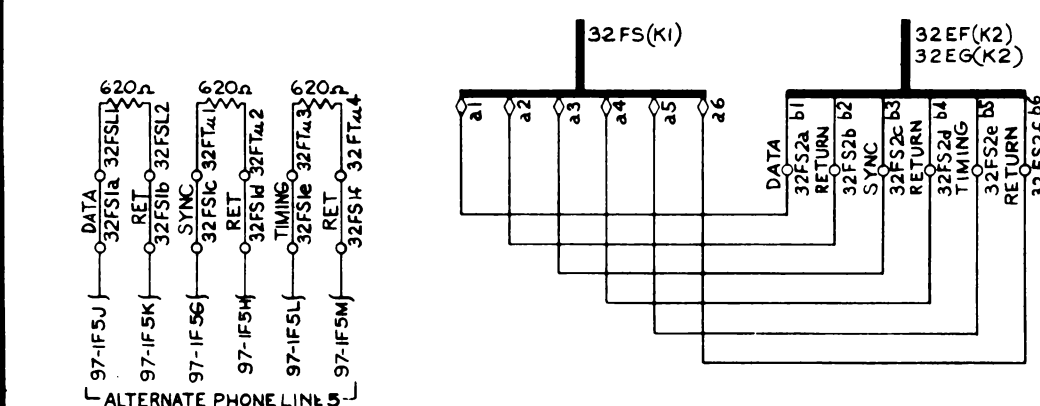
KINGSTON, NEW YORK

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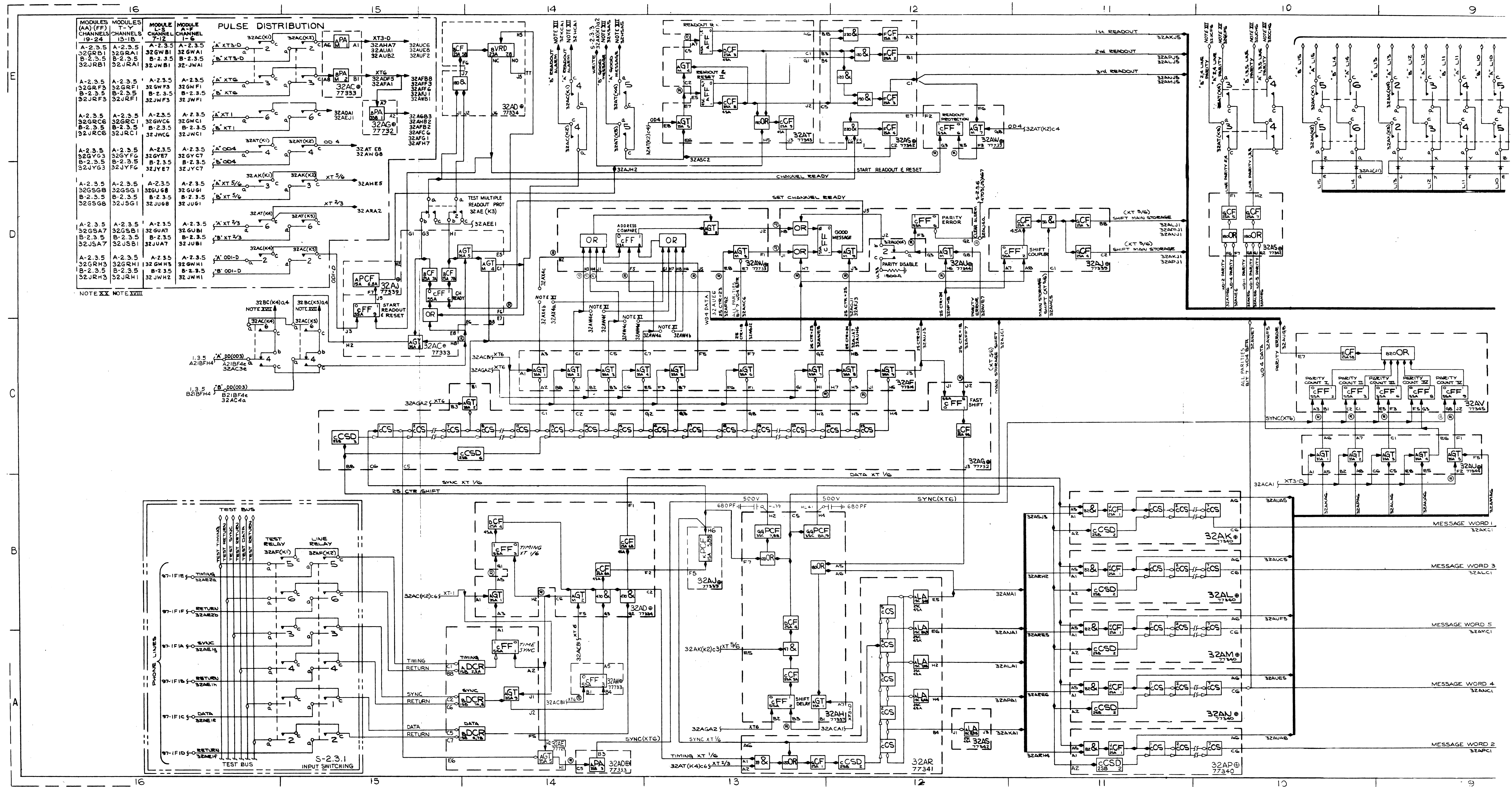


XIII THE CONNECTIONS SHOWN ARE FOR A SINGLE SITE (I.E. DC OR CC ALONE) USING ALTERNATE PHONE LINE 5. FOR A COMBINED SITE (I.E. DC AND CC) NOT USING ALTERNATE PHONE LINE 5 THESE CONNECTIONS SHOULD BE MOVED TO THE CORRESPONDING PINS OF THE OPPOSITE EDGE CONNECTORS AS SHOWN BELOW.



ALL RELAYS SHOWN HAVE 620 OHM RESISTORS, ON AN ADJACENT RESISTOR BOARD, CONNECTED BETWEEN b1 & b2, b3 & b4 AND b5 & b6, EXCEPT 32EF(K2) AND 32EG(K2).

XTEL INPUT SWITCHING



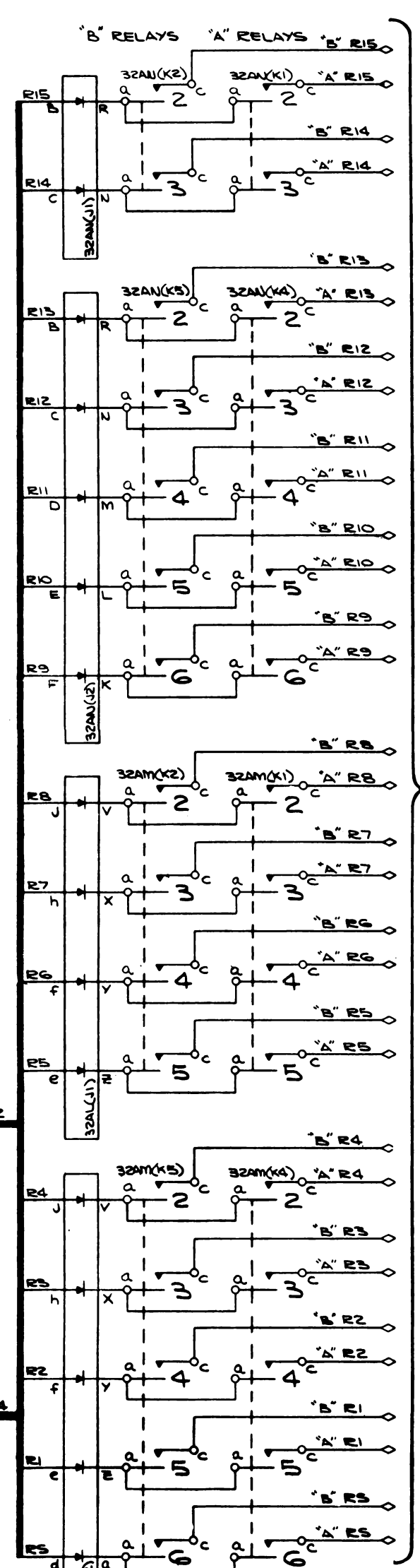
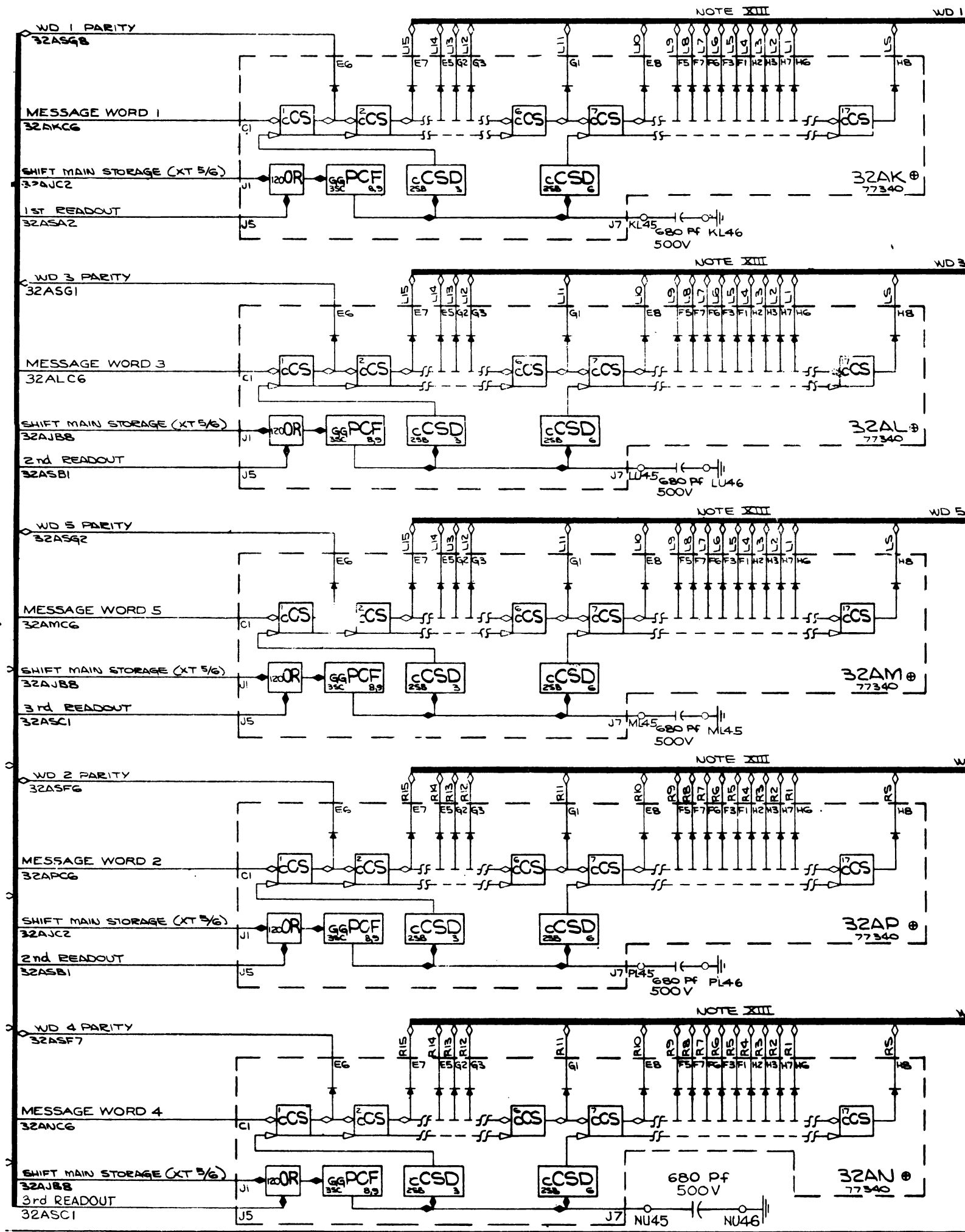
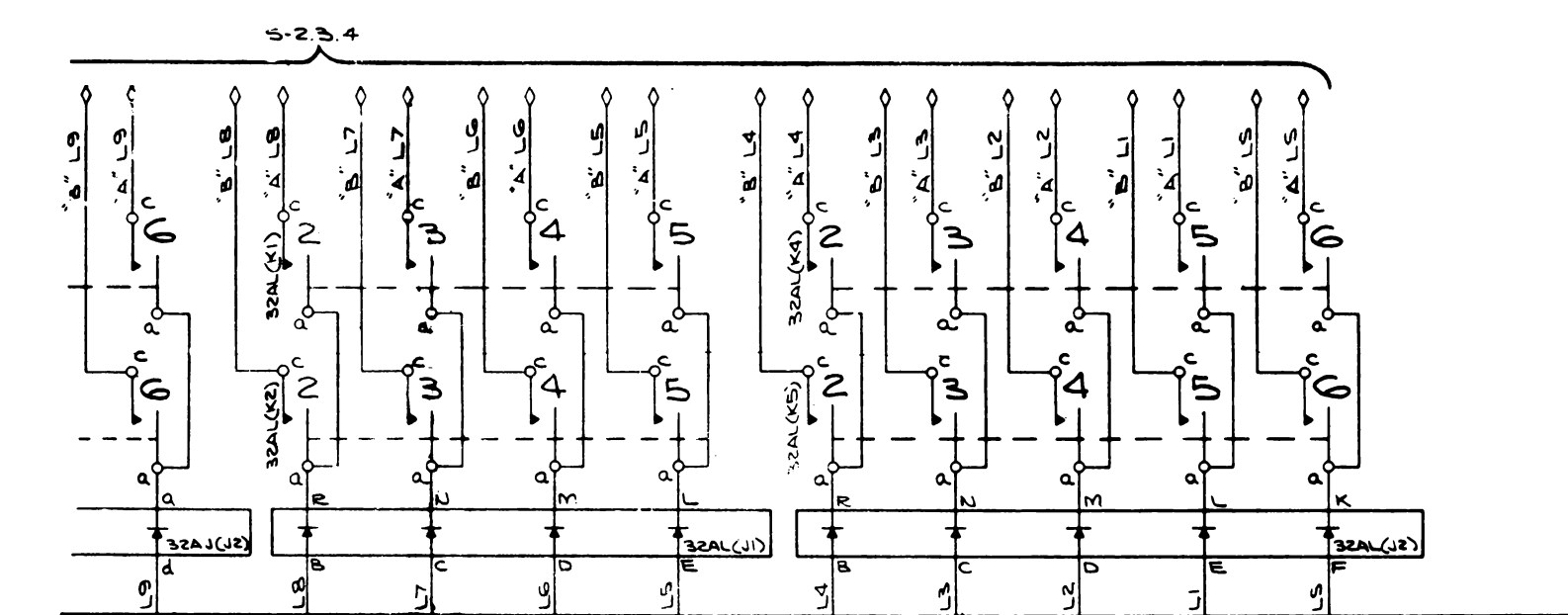


CHART VII

		'A' READOUT ALARM					'B' READOUT ALARM				
	MOD	CH	ORIGIN	EC#1	EC#2	TERM	ORIGIN	EC#1	EC#2	TERM	
NOTE XVIII	T	13	TC(K1)c4	TC3a	HD1a	HC61	TC(K2)c4	TC3c	KD1a	KCG1	
	U	14	UC(K1)c4	UC3a	HD1c	HC62	UC(K2)c4	UC3c	KD1c	KCG2	
	V	15	VC(K1)c4	VC3a	HD1e	HC63	VC(K2)c4	VC3c	KD1e	KCG3	
NOTE XIX	W	16	WC(K1)c4	WC3a	HD2a	HC65	WC(K2)c4	WC3c	KD2a	KCG5	
	X	17	XC(K1)c4	XC3a	HD2c	HC66	XC(K2)c4	XC3c	KD2c	KCG6	
	Y	18	YC(K1)c4	YC3a	HD2e	HC67	YC(K2)c4	YC3c	KD2e	KCG7	
NOTE XX	(FF)	19	(FF)(K1)c4	(FF)3a	HD3a	HCJ1	(FF)(K2)c4	(FF)3c	KD3a	KCJ1	
	(EE)	20	(EE)(K1)c4	(EE)3a	HD3c	HCJ2	(EE)(K2)c4	(EE)3c	KD3c	KCJ2	
	(DD)	21	(DD)(K1)c4	(DD)3a	HD3e	HCJ3	(DD)(K2)c4	(DD)3c	KD3e	KCJ3	
NOTE XXI	(CC)	22	(CC)(K1)c4	(CC)3a	HD4a	HCJ5	(CC)(K2)c4	(CC)3c	KD4a	KCJ5	
	(BB)	23	(BB)(K1)c4	(BB)3a	HD4c	HCJ6	(BB)(K2)c4	(BB)3c	KD4c	KCJ6	
	(AA)	24	(AA)(K1)c4	(AA)3a	HD4e	HCJ7	(AA)(K2)c4	(AA)3c	KD4e	KCJ7	

CHART VIII

		'A' 2,4 LINE PARITY					'B' 2,4 LINE PARITY				
	MOD	CH	ORIGIN	EC#1	EC#2	TERM	ORIGIN	EC#1	EC#2	TERM	
NOTE XVIII	T	13	TT(K4)a3	TU1a	GF1a	GGF6	TT(K5)a3	TU1a	KG3a	JGF6	
	U	14	UT(K4)a3	U11a	GF1c	GGF7	UT(K5)a3	U11a	KG3c	JGF7	
	V	15	VT(K4)a3	V11a	GF1e	GGF8	VT(K5)a3	V11a	KG3e	JGF8	
NOTE XIX	W	16	WT(K4)a3	W11a	GF1a	GGF9	WT(K5)a3	W11a	KG4a	JGF9	
	X	17	XT(K4)a3	X11a	GF1c	GGF10	XT(K5)a3	X11a	KG4c	JGF10	
	Y	18	YT(K4)a3	Y11a	GF1e	GGF11	YT(K5)a3	Y11a	KG4e	JGF11	
NOTE XX	(FF)	19	(FF)(K4)a3	(FF)11a	GF1a	GGF12	(FF)(K5)a3	(FF)11a	KF1a	JGF12	
	(EE)	20	(EE)(K4)a3	(EE)11a	GF1c	GGF13	(EE)(K5)a3	(EE)11a	KF1c	JGF13	
	(DD)	21	(DD)(K4)a3	(DD)11a	GF1e	GGF14	(DD)(K5)a3	(DD)11a	KF1e	JGF14	
NOTE XXI	(CC)	22	(CC)(K4)a3	(CC)11a	GF1a	GGF15	(CC)(K5)a3	(CC)11a	KG1a	JGF15	
	(BB)	23	(BB)(K4)a3	(BB)11a	GF1c	GGF16	(BB)(K5)a3	(BB)11a	KG1c	JGF16	
	(AA)	24	(AA)(K4)a3	(AA)11a	GF1e	GGF17	(AA)(K5)a3	(AA)11a	KG1e	JGF17	

CHART IX

										'A' 1,3,5 LINE PARITY										'B' 1,3,5 LINE PARITY																	
		MOD	CH	ORIGIN	EC#1	EC#2	TERM	ORIGIN	EC#1	EC#2	TERM			MOD	CH	ORIGIN	EC#1	EC#2	TERM			MOD	CH	ORIGIN	EC#1	EC#2	TERM										
NOTE XVIII		T	13	TT(K4)a4	TU1a	GF2a	GGH6	TT(K5)a4	TU1a	KG3a	JGH6			U	14	UT(K4)a4	U11a	GF2c	GGH7	UT(K5)a4	U11a	KG3c	JGH7			V	15	VT(K4)a4	V11a	GF2e	GGH8	VT(K5)a4	V11a	KG4a	JGH8		
		W	16	WT(K4)a4	W11a	GF2a	GGH9	WT(K5)a4	W11a	KG4a	JGH9			X	17	XT(K4)a4	X11a	GF2c	GGH10	XT(K5)a4	X11a	KG4c	JGH10			Y	18	YT(K4)a4	Y11a	GF2e	GGH11	YT(K5)a4	Y11a	KG4e	JGH11		
		(FF)	19	(FF)(K4)a4	(FF)11a	GF2a	GGH12	(FF)(K5)a4	(FF)11a	KF2a	JGH12			(EE)	20	(EE)(K4)a4	(EE)11a	GF2c	GGH13	(EE)(K5)a4	(EE)11a	KF2c	JGH13			(DD)	21	(DD)(K4)a4	(DD)11a	GF2e	GGH14	(DD)(K5)a4	(DD)11a	KF2e	JGH14		
		(CC)	22	(CC)(K4)a4	(CC)11a	GF2a	GGH15	(CC)(K5)a4	(CC)11a	KG2a	JGH15			(BB)	23	(BB)(K4)a4	(BB)11a	GF2c	GGH16	(BB)(K5)a4	(BB)11a	KG2c	JGH16			(AA)	24	(AA)(K4)a4	(AA)11a	GF2e	GGH17	(AA)(K5)a4	(AA)11a	KG2e	JGH17		
NOTE XIX																																					
NOTE XX																																					
NOTE XXI																																					

CHART X

'A' GOOD MESSAGE						'B' GOOD MESSAGE					
	MOD	CH	ORIGIN	EC#1	EC#2	TERM	ORIGIN	EC#1	EC#2	TERM	
NOTE XVIII	T	13	TT(K4)a5	TT2a	HX1a	HXA1	TT(K5)a5	TT2a	KX1a	KXA1	No
	U	14	UT(K4)a5	UT2a	HX1c	HXA2	UT(K5)a5	UT2a	KX1c	KXA2	
	V	15	VT(K4)a5	VT2a	HX1e	HXA3	VT(K5)a5	VT2a	KX1e	KXA3	
	W	16	WT(K4)a5	WT2a	HX1a	HXA5	WT(K5)a5	WT2a	KX1a	KXA5	
NOTE XIX	X	17	XT(K4)a5	XT2a	HX1c	HXA6	XT(K5)a5	XT2a	KX1c	KXA6	No
	Y	18	YT(K4)a5	YT2a	HX1e	HXA7	YT(K5)a5	YT2a	KX1e	KXA7	
	(FF)	19	(FF)(K4)a5	(FF)2a	HX2a	HXA8	(FF)(K5)a5	(FF)2a	KX2a	KXA8	
NOTE XX	(EE)	20	(EE)(K4)a5	(EE)2a	HX2c	HXB1	(EE)(K5)a5	(EE)2a	KX2c	KXB1	No
	(DD)	21	(DD)(K4)a5	(DD)2a	HX2e	HXB2	(DD)(K5)a5	(DD)2a	KX2e	KXB2	
	(CC)	22	(CC)(K4)a5	(CC)2a	HX2a	HXB3	(CC)(K5)a5	(CC)2a	KX2a	KXB3	
NOTE XXI	(BB)	23	(BB)(K4)a5	(BB)2a	HX2c	HXB4	(BB)(K5)a5	(BB)2a	KX2c	KXB4	No
	(AA)	24	(AA)(K4)a5	(AA)2a	HX2e	HXB5	(AA)(K5)a5	(AA)2a	KX2e	KXB5	

EXPANDED SECTION T THROUGH (FF) CHARTS

CHART I

'A' READOUT ALARM					'B' READOUT ALARM				
MOD	CH	ORIGIN	EC#1	EC#2	TERM	ORIGIN	EC#1	EC#2	TERM
NOTE XVIII	A	AC(K1)a4	EC1a		HC41	AC(K2)a4	EC1a	HC41	KCA1
	B	BC(K1)a4	EC2a		HC42	BC(K2)a4	EC2c	HC42	KCA2
	C	CC(K1)a4	EC2e		HC43	CC(K2)a4	EC2c	HC43	KCA3
NOTE XIX	D	DC(K1)a4	EC3a		HC45	DC(K2)a4	EC3c	HC45	KCA5
	E	EC(K1)a4	EC3e		HC46	EC(K2)a4	EC3c	HC46	KCA6
	F	FC(K1)a4	EC3a		HC47	FC(K2)a4	EC3c	HC47	KCA7
NOTE XX	L	LC(K1)a4	EC3a		HC48	LC(K2)a4	EC3c	HC48	KCA8
	M	MC(K1)a4	EC3c		HC49	MC(K2)a4	EC3c	HC49	KCA9
	N	NC(K1)a4	EC3e		HC50	NC(K2)a4	EC3c	HC50	KCA10
NOTE XXI	P	PC(K1)a4	EC3a		HC51	PC(K2)a4	EC3c	HC51	KCB1
	R	RC(K1)a4	EC3c		HC52	RC(K2)a4	EC3c	HC52	KCB2
	S	SC(K1)a4	EC3e		HC53	SC(K2)a4	EC3c	HC53	KCB3

CHART III

'A' 2,4 LINE PARITY					'B' 2,4 LINE PARITY				
MOD	CH	ORIGIN	EC#1	EC#2	TERM	ORIGIN	EC#1	EC#2	TERM
A	1	AT(K4)a5	ET1a		GC6a	AT(K5)a5	ET1c	JT1a	JCF6
B	2	BT(K4)a5	ET1c		GC6b	BT(K5)a5	ET1g	JT1c	JCF7
C	3	CT(K4)a5	ET1e		GC6c	CT(K5)a5	ET2c	JT1e	JCF8
D	4	DT(K4)a5	ET2a		GC6d	DT(K5)a5	ET2g	JT1g	JCF9
E	5	ET(K4)a5			GC1	ET(K5)a5	JT2a		JCF1
F	6	FT(K4)a5			GC2	FT(K5)a5	JT2c		JCF2
L	7	LT(K4)a5	JT2g		GC3	LT(K5)a5			JCF3
M	8	MT(K4)a5	JT3a		GC1	MT(K5)a5			JCG1
N	9	NT(K4)a5	JT1a	HT3c	GC2	JT(K5)a5	JT1c		JCG2
P	10	PT(K4)a5	JT1c	HT3e	GC3	PT(K5)a5	JT1g		JCG3
R	11	RT(K4)a5	JT1e	HT3g	GC1	RT(K5)a5	JT2c		JCH1
S	12	ST(K4)a5	JT2a	HT4a	GC2	ST(K5)a5	JT2g		JCE2

CHART VI CONT.

		FROM	TO		
NOTE XVIII	A	SC(K4)c6	SDIC		
		SDIC	TDIC		
		TDIC	TC(K4)a4		
		TC(K4)c6	UC(K4)a4		
		UC(K4)c6	VC(K4)a4		
		VC(K4)c6	VE3e		
		VE3e	WC(K4)a4		
		WC(K4)c6	XC(K4)a4		
		XC(K4)c6	YC(K4)a4		
		YC(K4)c6	YDIC		
		NOTE XIX	B	YDIC	(FF)DIC
				(FF)DIC	(FF)(K4)a4
(FF)(K4)c6	(EE)(K4)a4				
(EE)(K4)c6	OD(K4)a4				
OD(K4)c6	DD(K4)c6				
DD(K4)c6	CC(K4)a4				

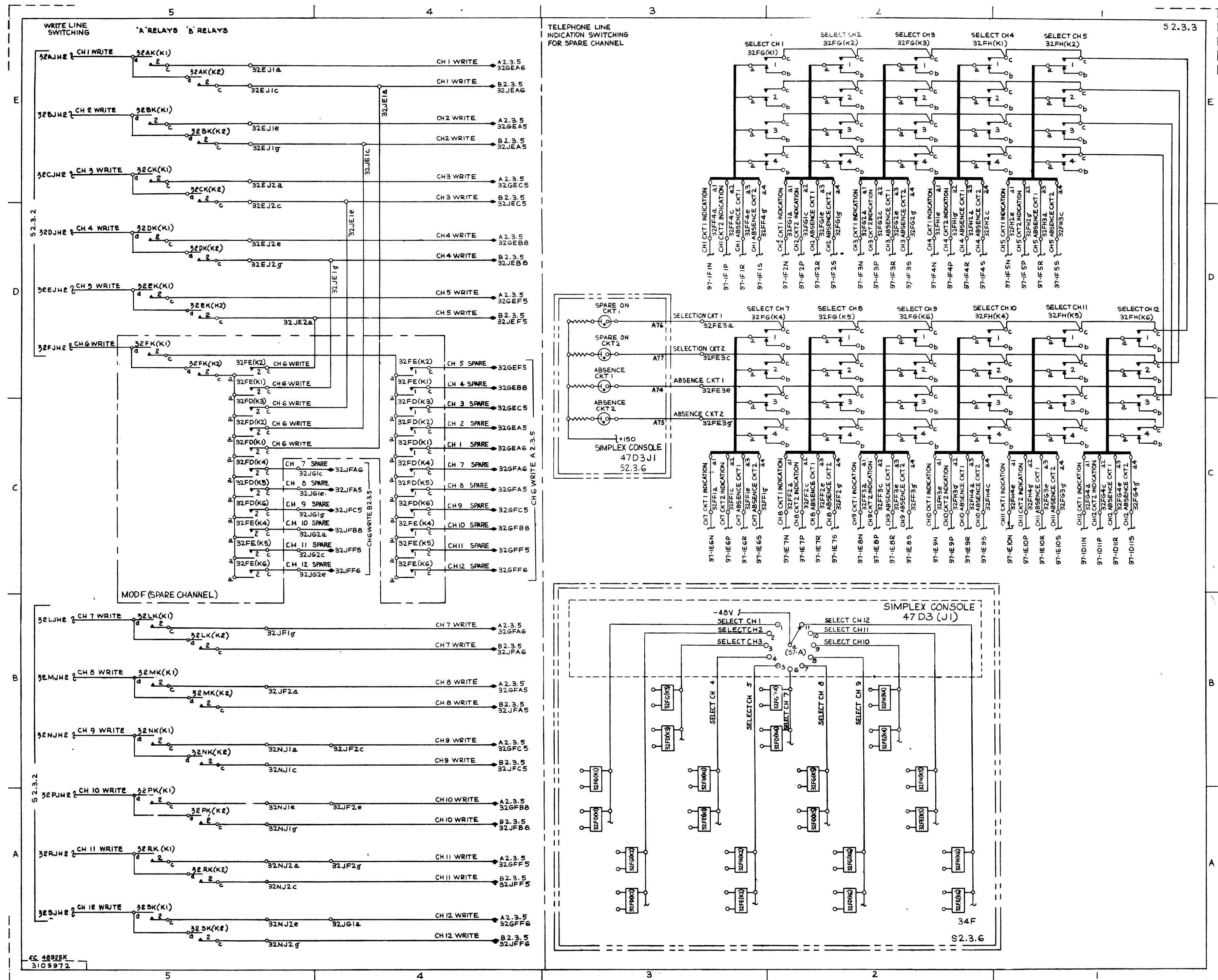
CHART VII

BIT NO.	JUMPERS BETWEEN			
	WORD 1	WORD 2	WORD 3	WORD 4
LS	17	KH8	LH8	
LI	16	KH6	LH6	
L2	15	KH7	LH7	
L3	14	KH3	LH3	
L4	13	KH2	LH2	
L5	12	KF1	LF1	
L6	11	KF3	LF3	
L7	10	KF6	LF6	
L8	9	KF7	LF7	
L9	8	KF5	LF5	
LK	7	KE8	LE8	
L11	6	KG1	LG1	
L12	5	KG3	LG3	

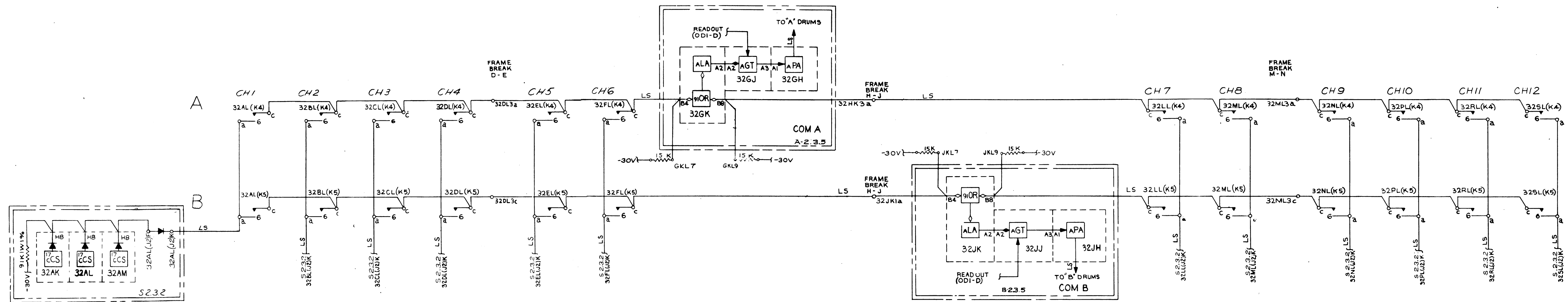
XVIII	C.B.	XX	SC(K5)c6 SD1g TD1g TC(K5)a4 UC(K5)a4 VC(K5)c6 VE3g WC(K5)a4 XC(K5)a4 YC(K5)a4 YD1g
-------	------	----	--

	(BB)(K5)G	(AA)(K5)4			
			RIS	2	PE7

I WHEN MODULES T THRU V AND (F) THRU (A) ARE SUPPLIED NOT
 II WHEN MODULES T THRU Y, (FF) (EE) AND (DD) ARE SUPPLIED NOT
 III WHEN MODULES T THRU Y ARE SUPPLIED NOTE XVIII AND XXII
 IV WHEN MODULES T,U AND V ARE SUPPLIED THIS WIRING IS APPL
 I FOR POINT TO POINT WIRING OF DQUM DEMAND BETWEEN MODUL
 II ALL DIODE CARDS (J1)(J2) HAVE PART NO.3091924
 III PART OF CHARTS V, CHARTS PLANTON ON S-2, 3, 4 AND 5
 IV THIS DRAWING IS FOR A COMPLETE UNIT 32 HAVING ALL MODULE
 I IS NOT SUPPLIED CHARTS VII THROUGH X AND XII CONT A
 II FOR POINT TO POINT WIRING FROM STORAGE REGISTER TO DI
 III THE DESIGNATION SHOWN IS FOR MODULE A, FOR MODULES B THRU S
 I READOUT ALARM - CHART I, VII
 II LINE PARITY 1,2,3 - CHART I, VII
 III LINE PARITY 2,4 - CHART III, VII
 I GOOD MESSAGE - CHART IV, X
 II EDGE CONNECTOR JUMPERS ARE USED TO ESTABLISH THE
 I FOR: 1st ADDRESS BIT I CONNECT 32AX4a TO 32AX4b
 I 1st ADDRESS BIT O CONNECT 32AX4c TO 32AX4d

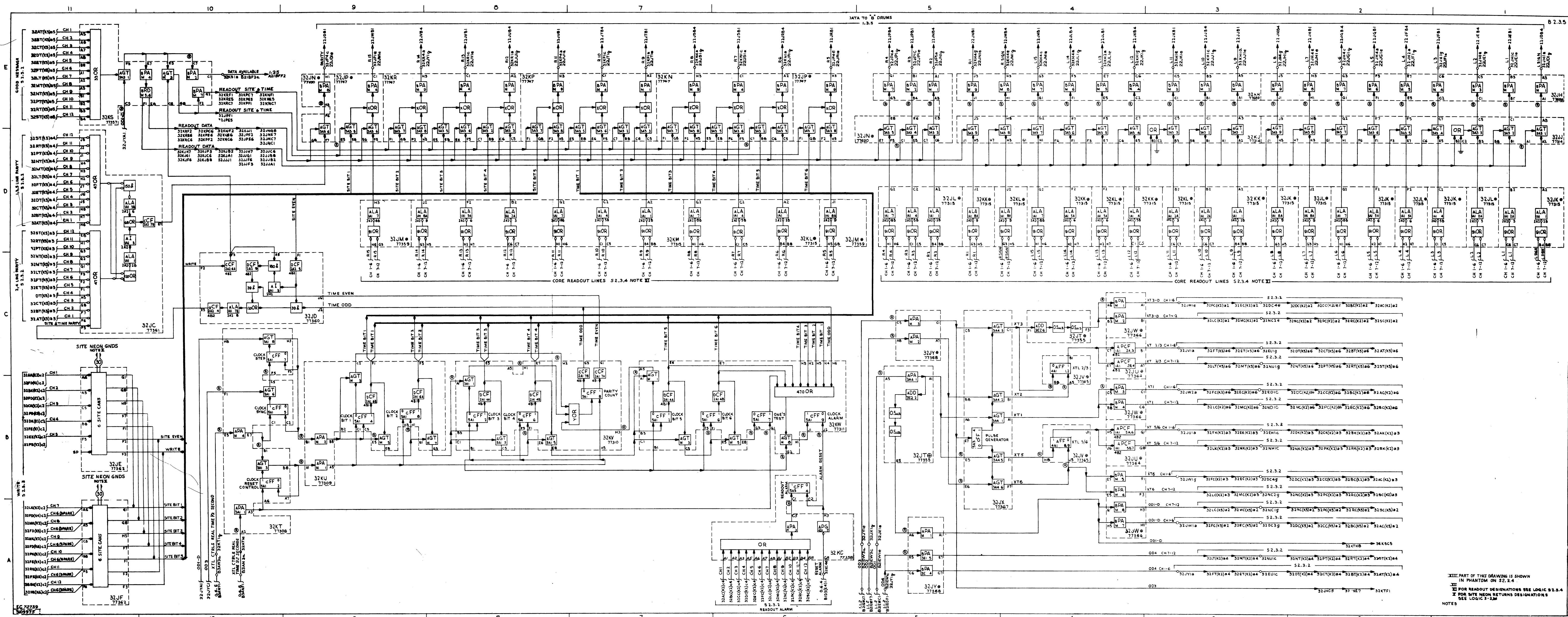


XTEL WRITE SW AND SPARE CHAN PHONE LINE INDICATION SW



A	CORE OUT PUT CH1	CORE OUT PUT CH 2	CORE OUT PUT CH 3	CORE OUT PUT CH 4	FRAME BREAK	CORE OUT PUT CH 5	CORE OUT PUT CH 6	CHI-6 COMMON A INPUT	15 K"OR" RES CH 1-6	15 K"OR" RES CH 7-12	CHI-12 COMMON A INPUT	FRAME BREAK	CORE OUT PUT CH 7	CORE OUT PUT CH 8	FRAME BREAK	CORE OUT PUT CH 9	CORE OUT PUT CH 10	CORE OUT PUT CH 11	CORE OUT PUT CH 12
LS	AL(K4)c6	BL(K4)c6	CL(K4)c6	DL(K4)c6	DL3a	EL(K4)c6	FL(K4)c6	GKB4	GKL7	GKL9	GK8B	HK3a	LL(K4)c6	ML(K4)c6	ML3a	NL(K4)c6	PL(K4)c6	RL(K4)c6	SL(K4)c6
L1	AL(K4)c5	BL(K4)c5	CL(K4)c5	DL(K4)c5	DL3e	EL(K4)c5	FL(K4)c5	GKC6	GKL15	GKL17	GK7	HK3c	LL(K4)c5	ML(K4)c5	ML3e	NL(K4)c5	PL(K4)c5	RL(K4)c5	SL(K4)c5
L2	AL(K4)c4	BL(K4)c4	CL(K4)c4	DL(K4)c4	DL4a	EL(K4)c4	FL(K4)c4	GLC6	GLU15	GLU17	GLC7	HL3a	LL(K4)c4	ML(K4)c4	ML4a	NL(K4)c4	PL(K4)c4	RL(K4)c4	SL(K4)c4
L3	AL(K4)c3	BL(K4)c3	CL(K4)c3	DL(K4)c3	DL4e	EL(K4)c3	FL(K4)c3	GKC1	GKL11	GKL13	GKC5	HK3e	LL(K4)c3	ML(K4)c3	ML4e	NL(K4)c3	PL(K4)c3	RL(K4)c3	SL(K4)c3
L4	AL(K4)c2	BL(K4)c2	CL(K4)c2	DL(K4)c2	EL1a	EL(K4)c2	FL(K4)c2	GLH2	GLU37	GLU43	GLH7	HL3c	LL(K4)c2	ML(K4)c2	NL1a	NL(K4)c2	PL(K4)c2	RL(K4)c2	SL(K4)c2
L5	AL(K4)c1	BL(K4)c1	CL(K4)c1	DL(K4)c1	EL1e	EL(K4)c1	FL(K4)c1	GKH2	GKL37	GKL43	GKH7	HK3g	LL(K4)c1	ML(K4)c1	NL1e	NL(K4)c1	PL(K4)c1	RL(K4)c1	SL(K4)c1
L6	AL(K4)c0	BL(K4)c0	CL(K4)c0	DL(K4)c0	EL2a	EL(K4)c0	FL(K4)c0	GKH1	GKL35	GKL41	GKH6	HK4a	LL(K4)c0	ML(K4)c0	NL2a	NL(K4)c0	PL(K4)c0	RL(K4)c0	SL(K4)c0
L7	AL(K4)c3	BL(K4)c3	CL(K4)c3	DL(K4)c3	EL2e	EL(K4)c3	FL(K4)c3	GLG3	GLU33	GLU39	GLH5	HL3e	LL(K4)c3	ML(K4)c3	NL2e	NL(K4)c3	PL(K4)c3	RL(K4)c3	SL(K4)c3
L8	AL(K4)c2	BL(K4)c2	CL(K4)c2	DL(K4)c2	EK2e	EL(K4)c2	FL(K4)c2	GKG3	GKL33	GKL39	GKH5	HK4c	LL(K4)c2	ML(K4)c2	NK2e	NL(K4)c2	PL(K4)c2	RL(K4)c2	SL(K4)c2
L9	AK(K4)c6	BK(K4)c6	CK(K4)c6	DK(K4)c6	DK3a	EK(K4)c6	FK(K4)c6	HKB4	HKL7	HKL9	HK8B	HK4e	LK(K4)c6	MK(K4)c6	MK3a	NK(K4)c6	PK(K4)c6	RK(K4)c6	SK(K4)c6
L10	AK(K4)c5	BK(K4)c5	CK(K4)c5	DK(K4)c5	DK3e	EK(K4)c5	FK(K4)c5	HKC6	HKL15	HKL17	HKC7	HK4g	LK(K4)c5	MK(K4)c5	MK3e	NK(K4)c5	PK(K4)c5	RK(K4)c5	SK(K4)c5
L11	AK(K4)c4	BK(K4)c4	CK(K4)c4	DK(K4)c4	DK4a	EK(K4)c4	FK(K4)c4	HL6c	HLU15	HLU17	HL7c	HL3g	LK(K4)c4	MK(K4)c4	MK4a	NK(K4)c4	PK(K4)c4	RK(K4)c4	SK(K4)c4
L12	AK(K4)c3	BK(K4)c3	CK(K4)c3	DK(K4)c3	DK4e	EK(K4)c3	FK(K4)c3	HKC1	HLK11	HLK13	HKC5	HJ3a	LK(K4)c3	MM(K4)c3	MK4e	NK(K4)c3	PK(K4)c3	RK(K4)c3	SK(K4)c3
L13	AK(K4)c2	BK(K4)c2	CK(K4)c2	DK(K4)c2	EK1a	EK(K4)c2	FK(K4)c2	HLH2	HLU39	HLU45	HLH7	HL4a	LK(K4)c2	MM(K4)c2	NK1a	NK(K4)c2	PK(K4)c2	RK(K4)c2	SK(K4)c2
L14	AK(K4)c6	BK(K4)c6	CK(K4)c6	DK(K4)c6	EK1e	EK(K4)c6	FK(K4)c6	HKH2	HKL37	HKL43	HKH7	HJ3c	LK(K4)c6	MK(K4)c6	NK1e	NK(K4)c6	PK(K4)c6	RK(K4)c6	SK(K4)c6
L15	AK(K4)c5	BK(K4)c5	CK(K4)c5	DK(K4)c5	EK2a	EK(K4)c5	FK(K4)c5	HKH1	HLK35	HLK41	HKH6	HJ3e	LK(K4)c5	MM(K4)c5	NK2a	NK(K4)c5	PK(K4)c5	RK(K4)c5	SK(K4)c5
RS	AM(K4)c6	BM(K4)c6	CM(K4)c6	DM(K4)c6	DM3a	EM(K4)c6	FM(K4)c6	HLG3	HLU35	HLU41	HLH5	HL4c	LM(K4)c6	MM(K4)c6	MM3a	NM(K4)c6	PM(K4)c6	RM(K4)c6	SM(K4)c6
R1	AM(K4)c5	BM(K4)c5	CM(K4)c5	DM(K4)c5	DM3e	EM(K4)c5	FM(K4)c5	HKG3	HLK33	HKL39	HKH5	HJ3g	LM(K4)c5	MM(K4)c5	MM3e	NM(K4)c5	PM(K4)c5	RM(K4)c5	SM(K4)c5
R2	AM(K4)c4	BM(K4)c4	CM(K4)c4	DM(K4)c4	DM4a	EM(K4)c4	FM(K4)c4	GLB4	GLU7	GLU9	GLB8	HL4e	LM(K4)c4	MM(K4)c4	MM4a	NM(K4)c4	PM(K4)c4	RM(K4)c4	SM(K4)c4
R3	AM(K4)c3	BM(K4)c3	CM(K4)c3	DM(K4)c3	DM4e	EM(K4)c3	FM(K4)c3	GLC1	GLU11	GLU13	GLC5	HM4g	LM(K4)c3	MM(K4)c3	MM4e	NM(K4)c3	PM(K4)c3	RM(K4)c3	SM(K4)c3
R4	AM(K4)c2	BM(K4)c2	CM(K4)c2	DM(K4)c2	EM1a	EM(K4)c2	FM(K4)c2	GLH1	GLU35	GLU41	GLH6	HL4g	LM(K4)c2	MM(K4)c2	NM1a	NM(K4)c2	PM(K4)c2	RM(K4)c2	SM(K4)c2
R5	AM(K4)c1	BM(K4)c1	CM(K4)c1	DM(K4)c1	EM1e	EM(K4)c1	FM(K4)c1	GMH5	GML37	GML35	GMG8	HM4a	LM(K4)c1	MM(K4)c1	NM1e	NM(K4)c1	PM(K4)c1	RM(K4)c1	SM(K4)c1
R6	AM(K4)c0	BM(K4)c0	CM(K4)c0	DM(K4)c0	EM2a	EM(K4)c0	FM(K4)c0	HLB4	HLU7	HLU9	HLB8	HM3c	LM(K4)c0	MM(K4)c0	NM2a	NM(K4)c0	PM(K4)c0	RM(K4)c0	SM(K4)c0
R7	AM(K4)c3	BM(K4)c3	CM(K4)c3	DM(K4)c3	EM2e	EM(K4)c3	FM(K4)c3	HLG1	HLU11	HLU13	HLG5	HM3e	LM(K4)c3	MM(K4)c3	NM2e	NM(K4)c3	PM(K4)c3	RM(K4)c3	SM(K4)c3
R8	AM(K4)c2	BM(K4)c2	CM(K4)c2	DM(K4)c2	EN2e	EM(K4)c2	FM(K4)c2	HLH1	HLU37	HLU43	HLH6	HM3g	LM(K4)c2	MM(K4)c2	NN2e	NM(K4)c2	PM(K4)c2	RM(K4)c2	SM(K4)c2
R9	AN(K4)c6	BN(K4)c6	CN(K4)c6	DN(K4)c6	DN3a	EN(K4)c6	FN(K4)c6	HMB4	HML9	HML11	HMB8	HM4a	LN(K4)c6	NN(K4)c6	MM3a	NN(K4)c6	PN(K4)c6	RN(K4)c6	SN(K4)c6
R10	AN(K4)c5	BN(K4)c5	CN(K4)c5	DN(K4)c5	DN3e	EN(K4)c5	FN(K4)c5	HMC1	HML13	HML15	HMC5	HM4c	LN(K4)c5	NN(K4)c5	MM3e	NN(K4)c5	PN(K4)c5	RN(K4)c5	SN(K4)c5
R11	AN(K4)c4	BN(K4)c4	CN(K4)c4	DN(K4)c4	DN4a	EN(K4)c4	FN(K4)c4	HMH1	HML37	HML43	HMH6	HM4e	LN(K4)c4	NN(K4)c4	MM4a	NN(K4)c4	PN(K4)c4	RN(K4)c4	SN(K4)c4
R12	AN(K4)c3	BN(K4)c3	CN(K4)c3	DN(K4)c3	DN4e	EN(K4)c3	FN(K4)c3	HMC6	HML17	HML19	HMC7	HM4g	LN(K4)c3	NN(K4)c3	MM4e	NN(K4)c3	PN(K4)c3	RN(K4)c3	SN(K4)c3
R13	AN(K4)c2	BN(K4)c2	CN(K4)c2	DN(K4)c2	EN1a	EN(K4)c2	FN(K4)c2	HMH2	HML39	HML45	HMH7	HN3a	LN(K4)c2	NN(K4)c2	NN1a	NN(K4)c2	PN(K4)c2	RN(K4)c2	SN(K4)c2
R14	AN(K4)c1	BN(K4)c1	CN(K4)c1	DN(K4)c1	EN1e	EN(K4)c1	FN(K4)c1	HMG3	HML35	HML41	HMH5	HN3c	LN(K4)c1	NN(K4)c1	NN1e	NN(K4)c1	PN(K4)c1	RN(K4)c1	SN(K4)c1
R15	AN(K4)c0	BN(K4)c0	CN(K4)c0	DN(K4)c0	EN2a	EN(K4)c0	FN(K4)c0	GMM6	GML39	GML33	GMG3	HN3e	LN(K4)c0	NN(K4)c0	NN2a	NN(K4)c0	PN(K4)c0	RN(K4)c0	SN(K4)c0





XIII PART OF THIS DRAWING IS SHOWN
IN PHANTOM ON S2.3.4
XII FOR READOUT DESIGNATIONS SEE LOGIC S2.3.4
XI FOR SITE NEON RETURNS DESIGNATIONS
SEE LOGIC S2.3.4
NOTES

CHART I									
		A' COMMON				B' COMMON			
LINE	CH	UNIT 32 RU PIN	UNIT 32 EDGE CONN	UNIT 47 CONN	UNIT 32 RU PIN	UNIT 32 EDGE CONN	UNIT 47 CONN	UNIT 32 RU PIN	UNIT 32 EDGE CONN
SITE BIT 1 NEON GND	2	32GEC1	32GE4	32JEC1	32JEC1	32JEC1	32JEC1	32JEC1	32JEC1
	3	32GEC2	32GE5	32JEC2	32JEC2	32JEC2	32JEC2	32JEC2	32JEC2
	4	32GEC3	32GE6	32JEC3	32JEC3	32JEC3	32JEC3	32JEC3	32JEC3
	5	32GEC4	32GE7	32JEC4	32JEC4	32JEC4	32JEC4	32JEC4	32JEC4
	6	32GEC5	32GE8	32JEC5	32JEC5	32JEC5	32JEC5	32JEC5	32JEC5
	7	32GEC6	32GE9	32JEC6	32JEC6	32JEC6	32JEC6	32JEC6	32JEC6
	8	32GEC7	32GE10	32JEC7	32JEC7	32JEC7	32JEC7	32JEC7	32JEC7
	9	32GEC8	32GE11	32JEC8	32JEC8	32JEC8	32JEC8	32JEC8	32JEC8
	10	32GEC9	32GE12	32JEC9	32JEC9	32JEC9	32JEC9	32JEC9	32JEC9
	11	32GEC10	32GE13	32JEC10	32JEC10	32JEC10	32JEC10	32JEC10	32JEC10
	12	32GEC11	32GE14	32JEC11	32JEC11	32JEC11	32JEC11	32JEC11	32JEC11
	13	32GEC12	32GE15	32JEC12	32JEC12	32JEC12	32JEC12	32JEC12	32JEC12
SITE BIT 2 NEON GND	2	32GEC13	32GE16	32JEC13	32JEC13	32JEC13	32JEC13	32JEC13	32JEC13
	3	32GEC14	32GE17	32JEC14	32JEC14	32JEC14	32JEC14	32JEC14	32JEC14
	4	32GEC15	32GE18	32JEC15	32JEC15	32JEC15	32JEC15	32JEC15	32JEC15
	5	32GEC16	32GE19	32JEC16	32JEC16	32JEC16	32JEC16	32JEC16	32JEC16
	6	32GEC17	32GE20	32JEC17	32JEC17	32JEC17	32JEC17	32JEC17	32JEC17
	7	32GEC18	32GE21	32JEC18	32JEC18	32JEC18	32JEC18	32JEC18	32JEC18
	8	32GEC19	32GE22	32JEC19	32JEC19	32JEC19	32JEC19	32JEC19	32JEC19
	9	32GEC20	32GE23	32JEC20	32JEC20	32JEC20	32JEC20	32JEC20	32JEC20
	10	32GEC21	32GE24	32JEC21	32JEC21	32JEC21	32JEC21	32JEC21	32JEC21
	11	32GEC22	32GE25	32JEC22	32JEC22	32JEC22	32JEC22	32JEC22	32JEC22
	12	32GEC23	32GE26	32JEC23	32JEC23	32JEC23	32JEC23	32JEC23	32JEC23
	13	32GEC24	32GE27	32JEC24	32JEC24	32JEC24	32JEC24	32JEC24	32JEC24
SITE BIT 3 NEON GND	2	32GEC25	32GE28	32JEC25	32JEC25	32JEC25	32JEC25	32JEC25	32JEC25
	3	32GEC26	32GE29	32JEC26	32JEC26	32JEC26	32JEC26	32JEC26	32JEC26
	4	32GEC27	32GE30	32JEC27	32JEC27	32JEC27	32JEC27	32JEC27	32JEC27
	5	32GEC28	32GE31	32JEC28	32JEC28	32JEC28	32JEC28	32JEC28	32JEC28
	6	32GEC29	32GE32	32JEC29	32JEC29	32JEC29	32JEC29	32JEC29	32JEC29
	7	32GEC30	32GE33	32JEC30	32JEC30	32JEC30	32JEC30	32JEC30	32JEC30
	8	32GEC31	32GE34	32JEC31	32JEC31	32JEC31	32JEC31	32JEC31	32JEC31
	9	32GEC32	32GE35	32JEC32	32JEC32	32JEC32	32JEC32	32JEC32	32JEC32
	10	32GEC33	32GE36	32JEC33	32JEC33	32JEC33	32JEC33	32JEC33	32JEC33
	11	32GEC34	32GE37	32JEC34	32JEC34	32JEC34	32JEC34	32JEC34	32JEC34
	12	32GEC35	32GE38	32JEC35	32JEC35	32JEC35	32JEC35	32JEC35	32JEC35
	13	32GEC36	32GE39	32JEC36	32JEC36	32JEC36	32JEC36	32JEC36	32JEC36

CHART II									
		GROUND		CIRCUIT 1		CIRCUIT 2		CIRCUIT 3	
CH	UNIT 47 PANEL LOC	UNIT 47 CONN A1	UNIT 47 CONN A2	UNIT 47 CONN A3	UNIT 47 CONN A4	UNIT 47 CONN A5	UNIT 47 CONN A6	UNIT 47 CONN A7	UNIT 47 CONN A8
2	47D3(1)	97IF 22	97IF 21	97IF 20	97IF 19	97IF 18	97IF 17	97IF 16	97IF 15
3	47D3(2)	97IF 32	97IF 31	97IF 30	97IF 29	97IF 28	97IF 27	97IF 26	97IF 25
4	47D3(3)	97IF 42	97IF 41	97IF 40	97IF 39	97IF 38	97IF 37	97IF 36	97IF 35
5	47D3(4)	97IF 52	97IF 51	97IF 50	97IF 49	97IF 48	97IF 47	97IF 46	97IF 45
6	47D3(5)	97IF 62	97IF 61	97IF 60	97IF 59	97IF 58	97IF 57	97IF 56	97IF 55
7	47D3(6)	97IF 72	97IF 71	97IF 70	97IF 69	97IF 68	97IF 67	97IF 66	97IF 65
8	47D3(7)	97IF 82	97IF 81	97IF 80	97IF 79	97IF 78	97IF 77	97IF 76	97IF 75
9	47D3(8)	97IF 92	97IF 91	97IF 90	97IF 89	97IF 88	97IF 87	97IF 86	97IF 85
10	47D3(9)	97IF 102	97IF 101	97IF 100	97IF 99	97IF 98	97IF 97	97IF 96	97IF 95
11	47D3(10)	97IF 112	97IF 111	97IF 110	97IF 109	97IF 108	97IF 107	97IF 106	97IF 105
12	47D3(11)	97IF 122	97IF 121	97IF 120	97IF 119	97IF 118	97IF 117	97IF 116	97IF 115
13	47D3(12)	97IF 132	97IF 131	97IF 130	97IF 129	97IF 128	97IF 127	97IF 126	97IF 125

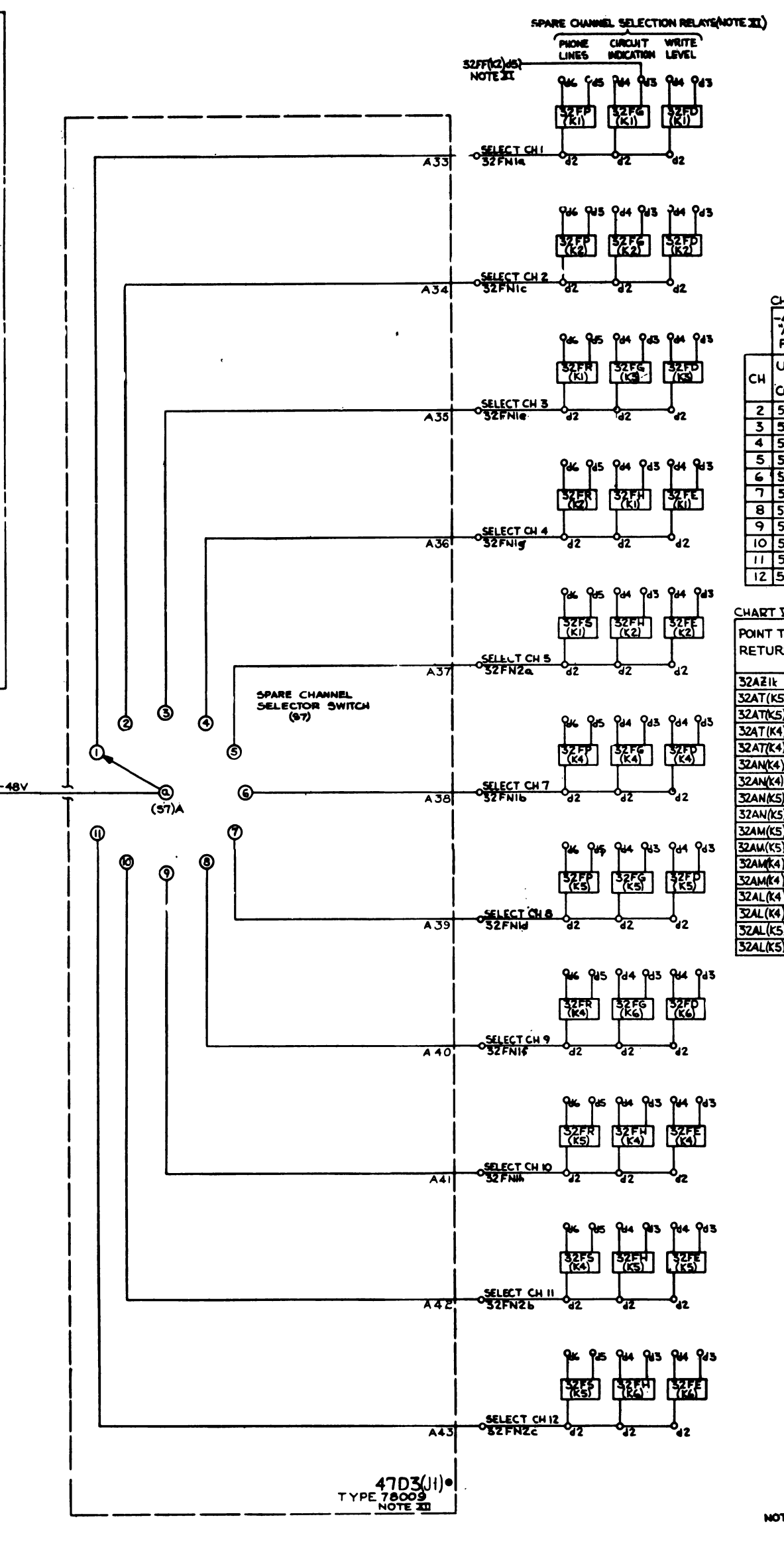
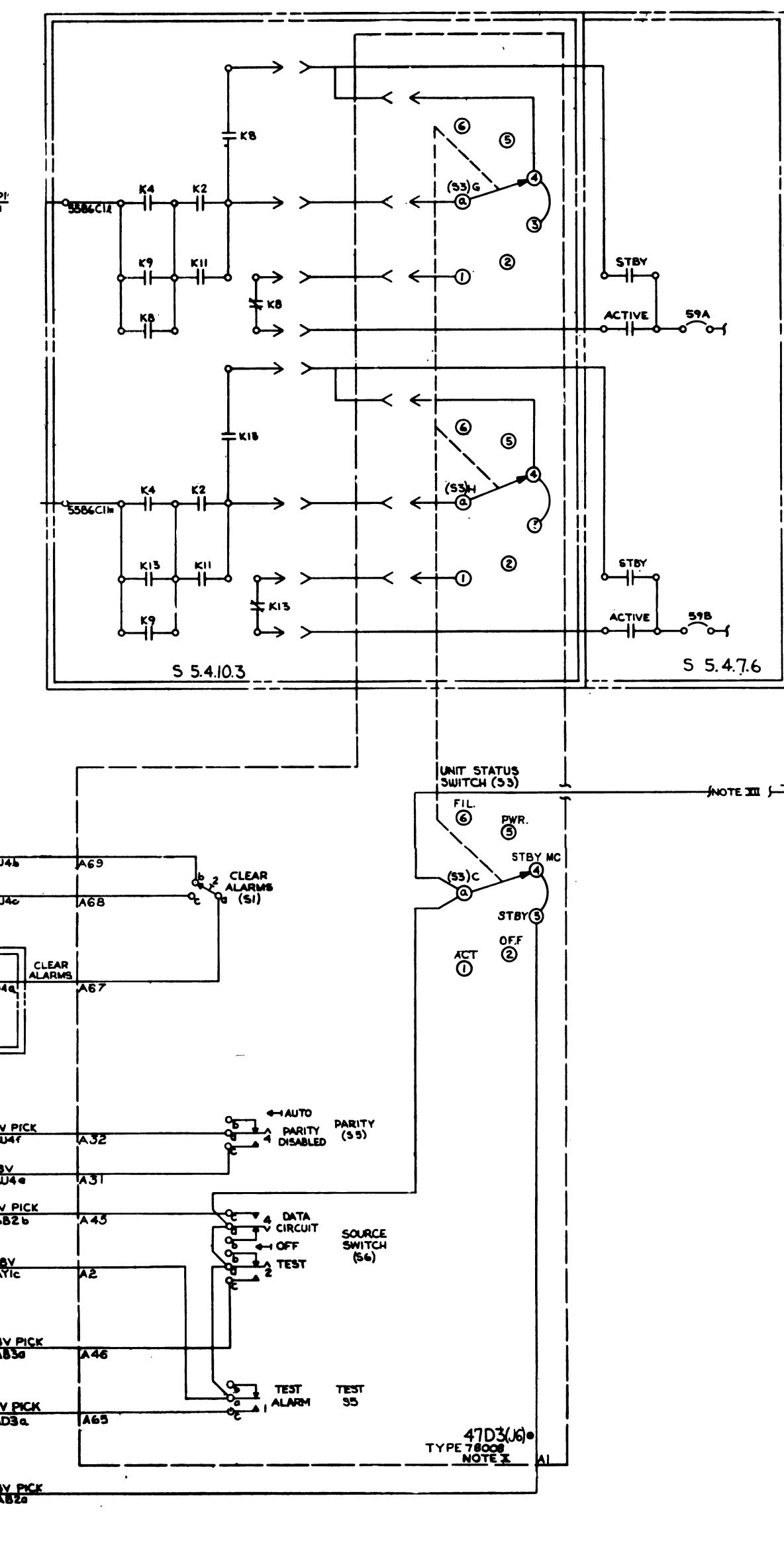
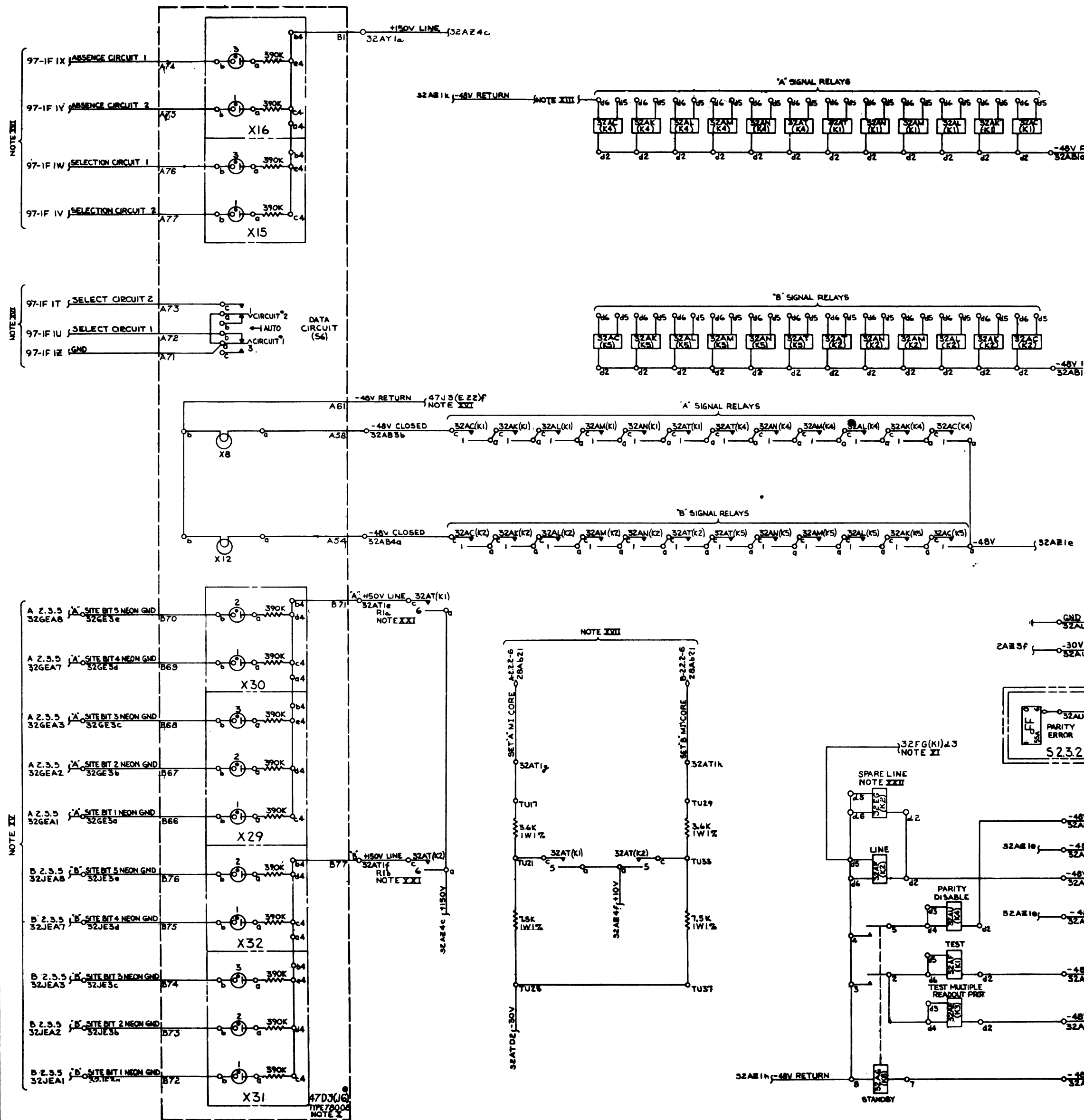
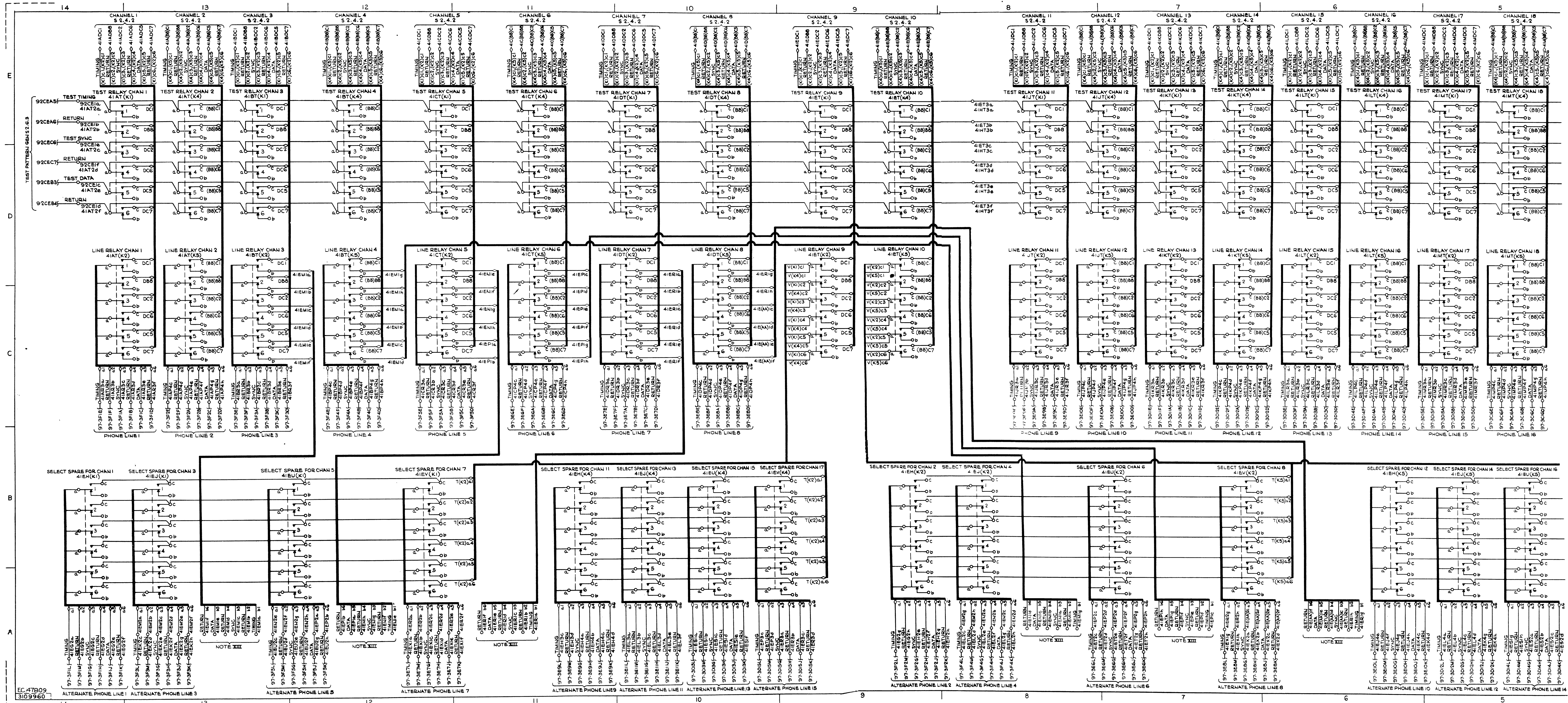
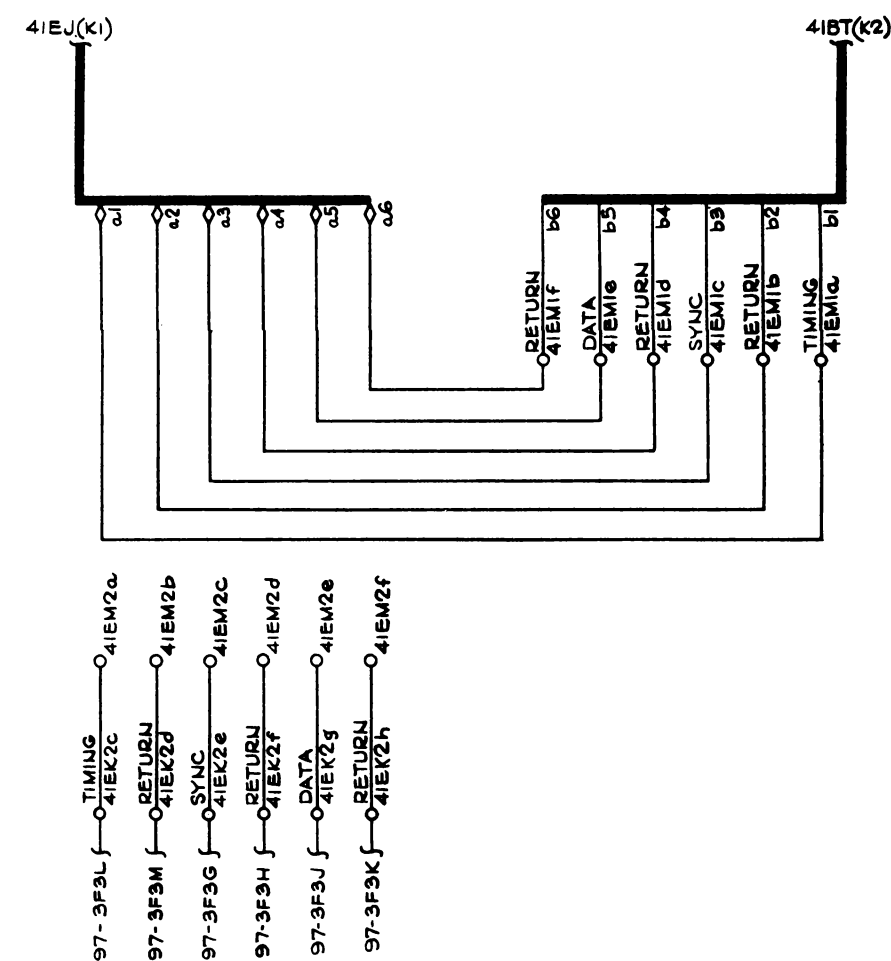
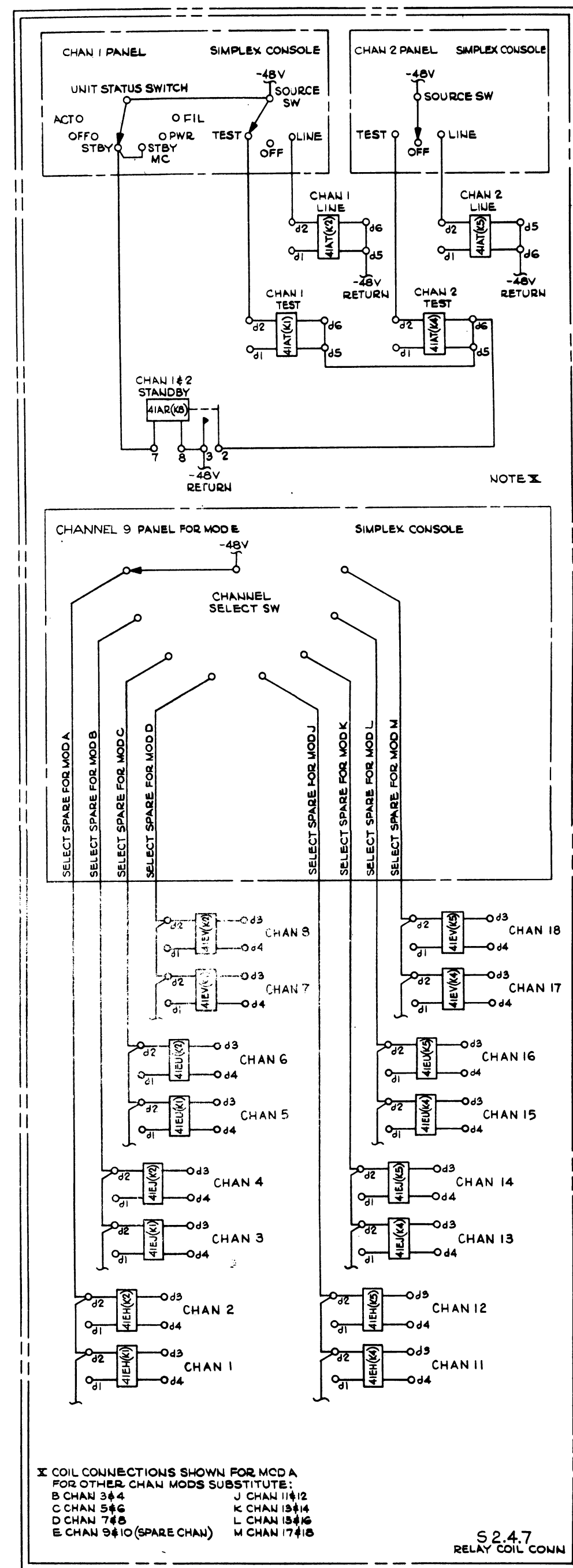
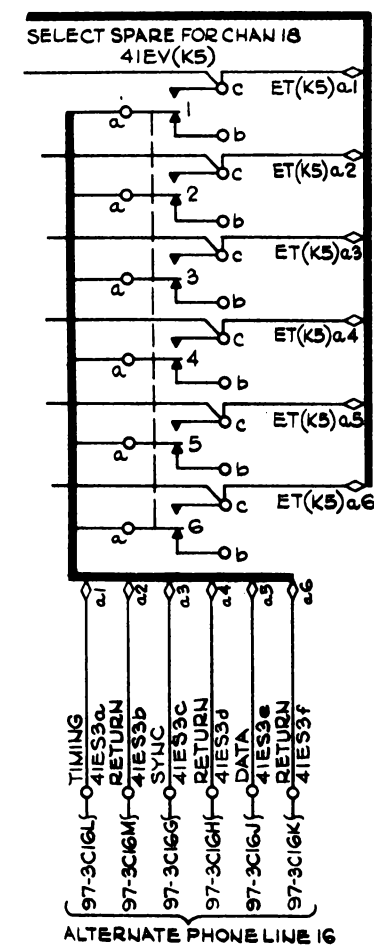
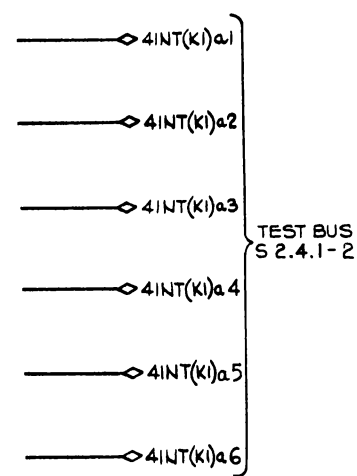
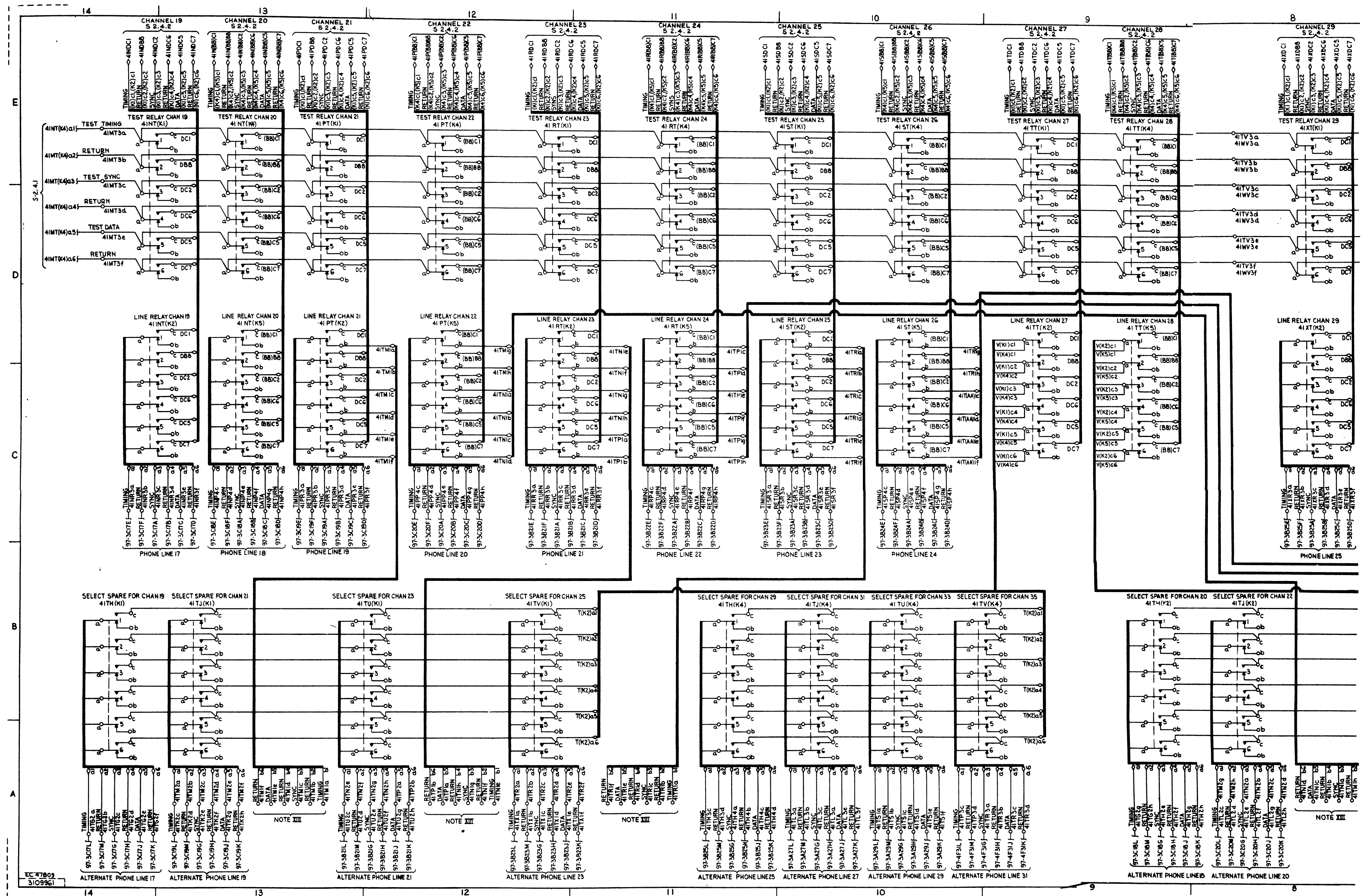


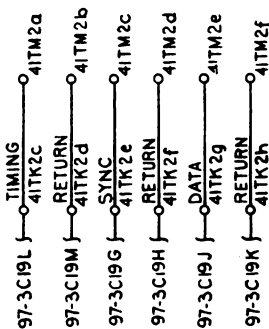
CHART III									
		SET 'A' CORE		SET 'B' CORE		SET 'C' CORE		SET 'D' CORE	
CH	UNIT 32 EDGE CONN	UNIT 32 PANEL CONN	UNIT 47 EDGE CONN	UNIT 47 PANEL CONN	UNIT 32 EDGE CONN	UNIT 32 PANEL CONN	UNIT 47 EDGE CONN	UNIT 47 PANEL CONN	UNIT 32 EDGE CONN
2	32GEC1	32GE4	32JEC1	32JEC1	32GEC1	32GE4	32JEC1	32JEC1	32GEC1
3	32GEC2	32GE5	32JEC2	32JEC2	32GEC2	32GE5	32JEC2	32JEC2	32GEC2
4	32GEC3	32GE6	32JEC3	32JEC3	32GEC3	32GE6	32JEC3	32JEC3	32GEC3
5	32GEC4	32GE7	32JEC4	32JEC4	32GEC4	32GE7	32JEC4	32JEC4	32GEC4
6	32GEC5	32GE8	32JEC5	32JEC5	32GEC5	32GE8	32JEC5	32JEC5	32GEC5
7	32GEC6	32GE9	32JEC6	32JEC6	32GEC6	32GE9	32JEC6	32JEC6	32GEC6
8	32GEC7	32GE10	32JEC7	32JEC7	32GEC7	32GE10	32JEC7	32JEC7	32GEC7
9	32GEC8	32GE11	32JEC8	32JEC8	32GEC8	32GE11	32JEC8	32JEC8	32GEC8
10	32GEC9	32GE12	32JEC9	32JEC9	32GEC9	32GE12	32JEC9	32JEC9	32GEC9
11	32GEC10	32GE13	32JEC10	32JEC10	32GEC10	32GE13	32JEC10	32JEC10	32GEC10
12	32GEC11	32GE14	32JEC11	32JEC11	32GEC11	32GE14	32JEC11	32JEC11	32GEC11





- XIII FOR SINGLE PHONE LINE OPERATION, DISCONNECT THE 620 Ω $\frac{1}{2}$ W RESISTORS FROM THE 'b' CONTACTS OF THE LINE RELAY IN THE CHANNEL WHICH IS TO OPERATE WITH A SINGLE PHONE LINE. THEN CHANGE THE LEADS IN THE SPARE MODULE AS ILLUSTRATED IN THE CHART ABOVE. (CHART SHOWS CHANNEL 9 AS A SAMPLE, OTHER CHANNELS WOULD BE WIRED IN A SIMILAR MANNER)
- XII PART OF THIS DRAWING SHOWN IN PHANTOM ON S 2.4.2
- XI THIS DRAWING IS FOR A COMPLETE UNIT 41 HAVING ALL MODULES WIRED. WHEN UNWIRED SHIPPING SECTION J-M IS SUBSTITUTED, THE CIRCUITRY SHOWN FOR THIS SHIPPING SECTION IS NOT APPLICABLE. WHEN J-M IS AN UNWIRED SHIPPING SECTION THE FOLLOWING SHIELDED TWISTED PAIR JUMPERS ARE PROVIDED.
- 41ET3a TO 41HT3a
41ET3b TO 41HT3b
41ET3c TO 41HT3c
41ET3d TO 41HT3d
41ET3e TO 41HT3e
41ET3f TO 41HT3f
- X ALL RELAYS SHOWN HAVE 620 Ω $\frac{1}{2}$ W RESISTORS CONNECTED BETWEEN b1 & b2, b3 & b4 AND b5 & b6.
- NOTES





NOTES

X COIL CONNECTIONS SHOWN FOR MOD N
FOR OTHER CHAN MODS SUBSTITUE
P CHAN 21 & 22 X CHAN
R CHAN 23 & 24 Y CHAN
S CHAN 25 & 26 (AA) CHAN
T CHAN 27 & 28 (SPARE CHAN) (BB) CHAN

S 2.4.7
RELAY COIL C



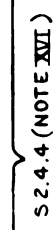


CHART II

LONGWAD " B "				
CH	RELAY	E C	PUPIN	
1	41AM(K)34	EP44	FC54	
2	41BM(K)35	EN44	FC54	
3	41BM(K)34	EN44	FC64	
4	41BM(K)35	EN44	FC54	
5	41CM(K)34	EN4C	FC24	
6	41CM(K)35	EN2C	FC24	
7	41DM(K)34	EN4E	FCB8	
8	41DM(K)35	EN04	FCB8	
9	41EM(K)34	EN49	FCB4	
10	41EM(K)35	EN39	FCB2	
11	41JM(K)34	HN44	FCB1	
12	41JM(K)35	HN34	FC48	
13	41KM(K)34	HN4C	FC47	
14	41MM(K)35	HN3C	FC47	
15	41LM(K)34	HN34	FC47	
16	41LM(K)35	HN34	FC42	
17	41MM(K)34	HN49	FC42	
18	41MM(K)35	HN39	FC41	
19	41NM(K)34	TP44	W44	
20	41NM(K)35	TP34	W4B4	
21	41PM(K)34	TN34	W4B4	
22	41PM(K)35	TN44	W4C2	
23	41RM(K)34	TN4C	W4C9	
24	41RM(K)35	TN3C	W4C2	
25	41SM(K)34	TN4E	W4F1	
26	41SM(K)35	TN3E	W4F1	
27	41TM(K)34	TN49	W4F1	
28	41TM(K)35	TN39	W4F1	
29	41YM(K)34	WN44	W4A2	
30	41XM(K)35	WN34	W4A2	
31	41YM(K)34	WN4C	W4A5	
32	41YM(K)35	WN3C	W4A5	
33	41(AA)M(K)34	WN4E	W4A7	
34	41(AA)M(K)35	WN3E	W4A8	
35	41(BB)M(K)34	WN49	W4B1	
36	41(BB)M(K)35	WN39	W4B2	

CHART IV

REKOUT ALARM " B "									
CH	RELAY	EC	EC	EC	EC	EC	EC	EC	EC
1	41A(M)5K5C2	FY2A	YJ2A	NY2A	UY2A	VH2A	WH2A	PH2A	CH2A
2	41A(M)5K5C2	FY1A	YJ1A	NY1A	UY1A	VH1A	WH1A	PH1A	CH1A
3	41B(M)5K5C2	FY3C	YJ3C	NY3C	UY3C	VH3C	WH3C	PH3C	CH3C
4	41B(M)5K5C2	FY1C	YJ1C	NY1C	UY1C	VH1C	WH1C	PH1C	CH1C
5	41C(M)5K5C2	FY2E	YJ2E	NY2E	UY2E	VH2E	WH2E	PH2E	CH2E
6	41C(M)5K5C3	FY1E	YJ1E	NY1E	UY1E	VH1E	WH1E	PH1E	CH1E
7	41D(M)5K5C2	FY2g	YJ2g	NY2g	UY2g	VH2g	WH2g	PH2g	CH2g
8	41D(M)5K5C3	FY1g	YJ1g	NY1g	UY1g	VH1g	WH1g	PH1g	CH1g
9	41E(M)5K5C2	FP2g	Jg	NP2g	UP2g	VH2g	WH2g	PH2g	CH2g
10	41E(M)5K5C3	FP1g	Jg	NP1g	UP1g	VH1g	WH1g	PH1g	CH1g
11	41J(M)5K5C2			NR2A	UP2A	VH2A	WH2A	PH2A	CH2A
12	41J(M)5K5C3			NR1A	UP1A	VH1A	WH1A	PH1A	CH1A
13	41K(M)5K5C2			NR2C	UP2C	VH2C	WH2C	PH2C	CH2C
14	41K(M)5K5C3			NR1C	UP1C	VH1C	WH1C	PH1C	CH1C
15	41L(M)5K5C2			NR2E	UP2E	VH2E	WH2E	PH2E	CH2E
16	41L(M)5K5C3			NR1E	UP1E	VH1E	WH1E	PH1E	CH1E
17	41M(M)5K5C2			NR2g	UP2g	VH2g	WH2g	PH2g	CH2g
18	41M(M)5K5C3			NR1g	UP1g	VH1g	WH1g	PH1g	CH1g
19	41N(M)5K5C2			UY2A	VH2A	WH2A	PH2A	CH2A	
20	41N(M)5K5C3			UY1A	VH1A	WH1A	PH1A	CH1A	
21	41P(M)5K5C2			UY2C	VH2C	WH2C	PH2C	CH2C	
22	41P(M)5K5C3			UY1C	VH1C	WH1C	PH1C	CH1C	
23	41Q(M)5K5C2			UY2E	VH2E	WH2E	PH2E	CH2E	
24	41R(M)5K5C3			UY1E	VH1E	WH1E	PH1E	CH1E	
25	41S(M)5K5C2			UY2g	VH2g	WH2g	PH2g	CH2g	
26	41S(M)5K5C3			UY1g	VH1g	WH1g	PH1g	CH1g	
27	41T(M)5K5C2			UW2A	VH2A	WH2A	PH2A	CH2A	
28	41T(M)5K5C3			UW1A	VH1A	WH1A	PH1A	CH1A	
29	41U(M)5K5C2			XW2A	VH2A	WH2A	PH2A	CH2A	
30	41X(M)5K5C3			XW1A	VH1A	WH1A	PH1A	CH1A	
31	41Y(M)5K5C2			XV2C	VH2C	WH2C	PH2C	CH2C	
32	41Y(M)5K5C3			XV1C	VH1C	WH1C	PH1C	CH1C	
33	41Z(M)5K5C2			XV2E	VH2E	WH2E	PH2E	CH2E	
34	41Z(M)5K5C3			XV1E	VH1E	WH1E	PH1E	CH1E	
35	41B(M)5K5C2			XV2g	VH2g	WH2g	PH2g	CH2g	
36	41B(M)5K5C3			XV1g	VH1g	WH1g	PH1g	CH1g	

NORMAL CHAN	SPARE CHAN
LK4	GK1
UK4	WK1
LK6	GK4
UK6	WK4

XI POINT TO POINT WIRING FOR -10 VOLTS ON LONG WORD DELAYS AM(K4) AND-AM(K5)
AS FOLLOWS: 1) AMEL-AM(K5)(C4-AM)(K5)(C5-AM)(K4)(C4-AM)(K5)(C4)

XX PART OF THIS DRAWING IN PHANTOM ON S2.4-A AND S2.4-B
DIODE CARDS J1 AND J2 ARE PART NUMBER 3091924.

III THE DELAYS ON THE SPACE CHANNEL ARE IN A DIFFERENT
LOCATION SEE CHART II FOR CONVERSION

XXXI SEE LOGIC S2.4.3 FOR POINTS TO POINT WIRING TO COMMON EQUIPMENT
DEVIATION FROM EQUIPMENT CHANGE FOR DIFFERENT MODULES

IIII SEE LOGIC S2.4.4 FOR POINT TO POINT WIRING AND DESTINATIONS

XXXX SEE LOGIC S2.4.7 FOR ORIGINS AND/OR DESTINATIONS.

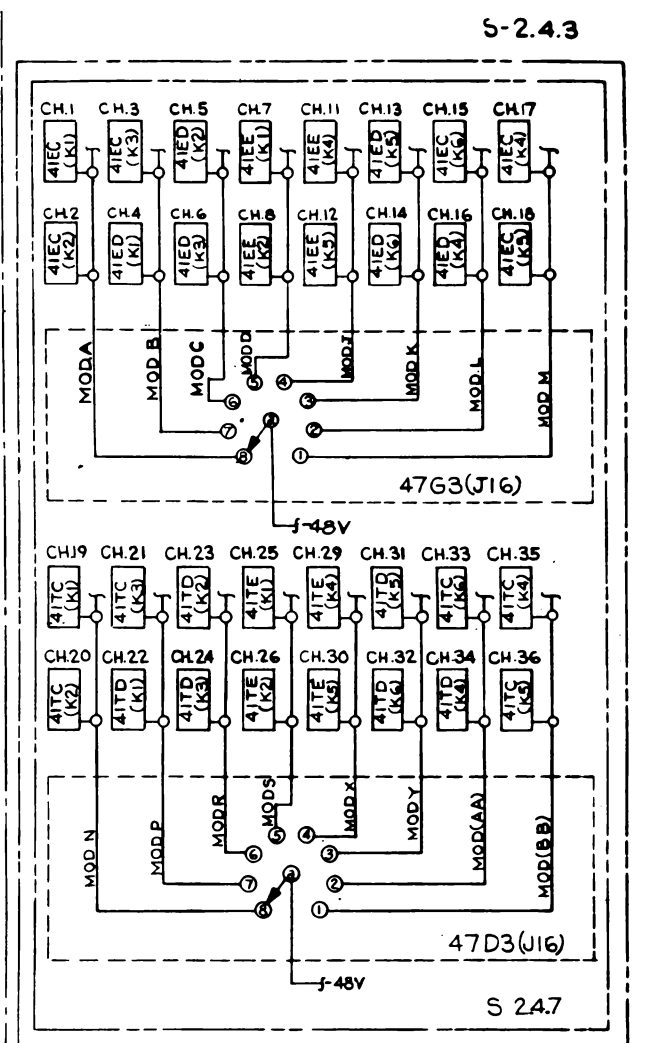
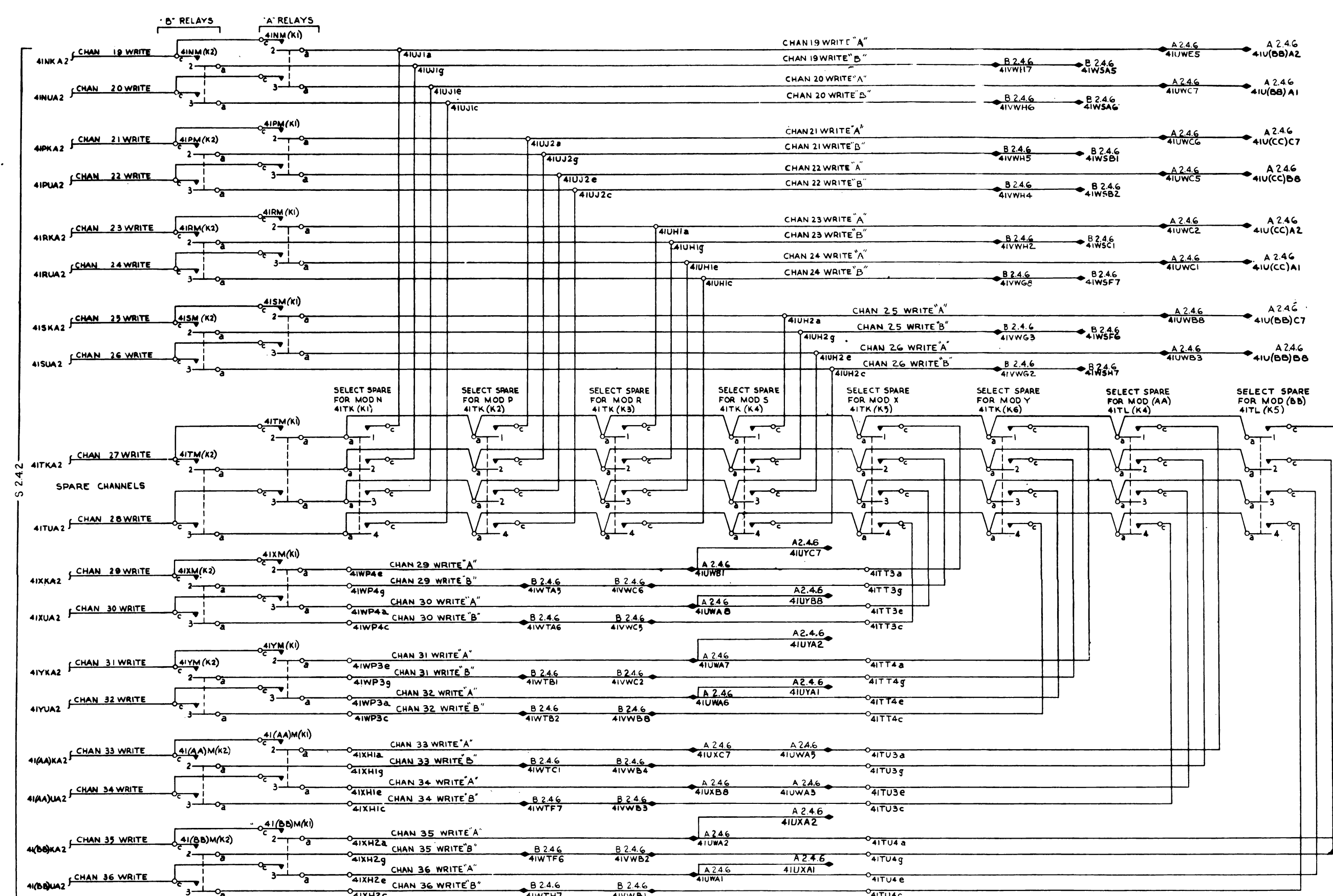
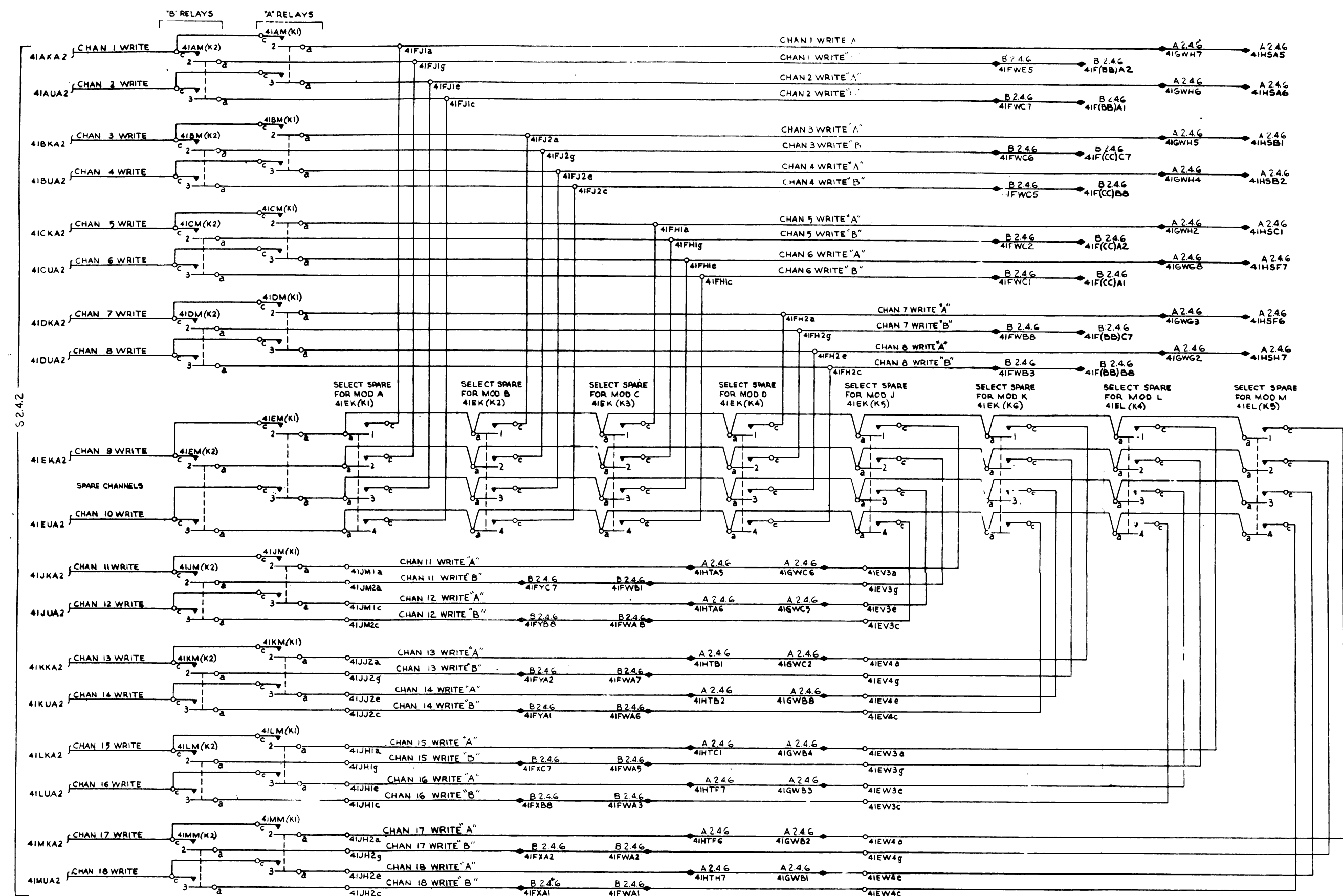
XXXXX SEE LOGIC S2.4.7 FOR ORIGIN AND EDGE CONNECTOR.

V FOR POINT TO POINT WIRING FROM STORAGE REGISTERS TO DIODE

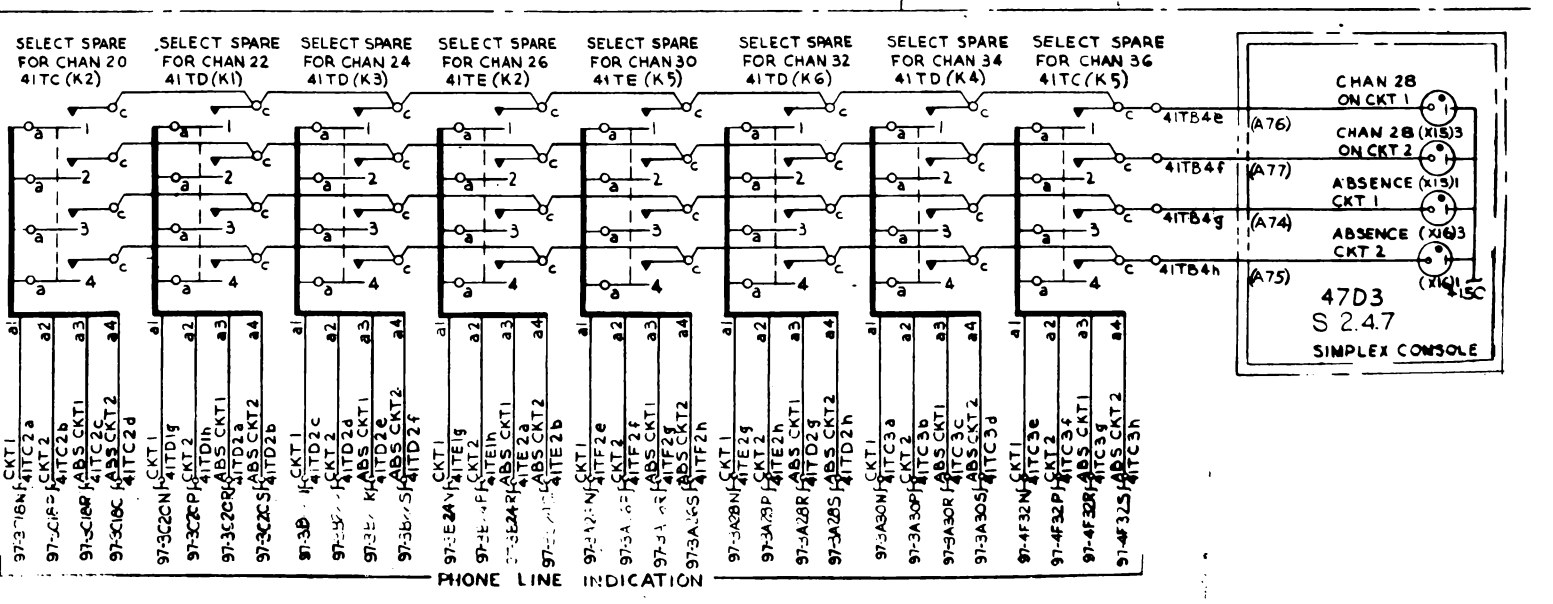
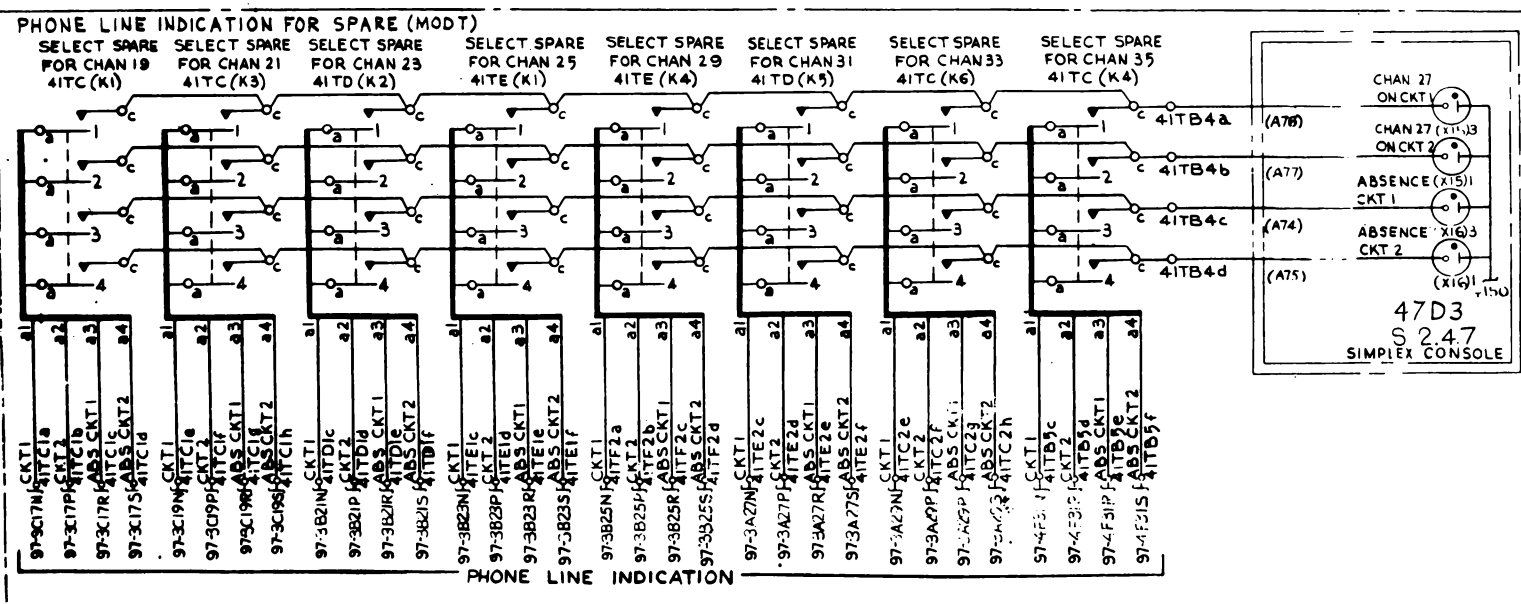
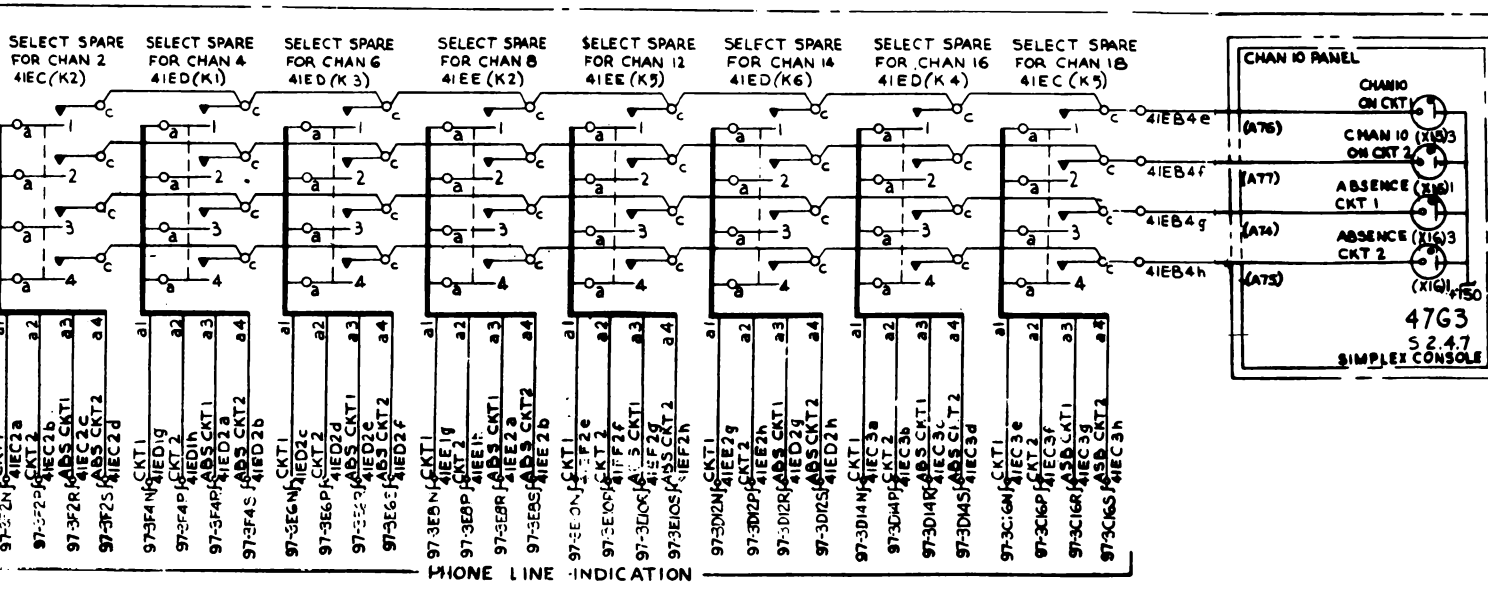
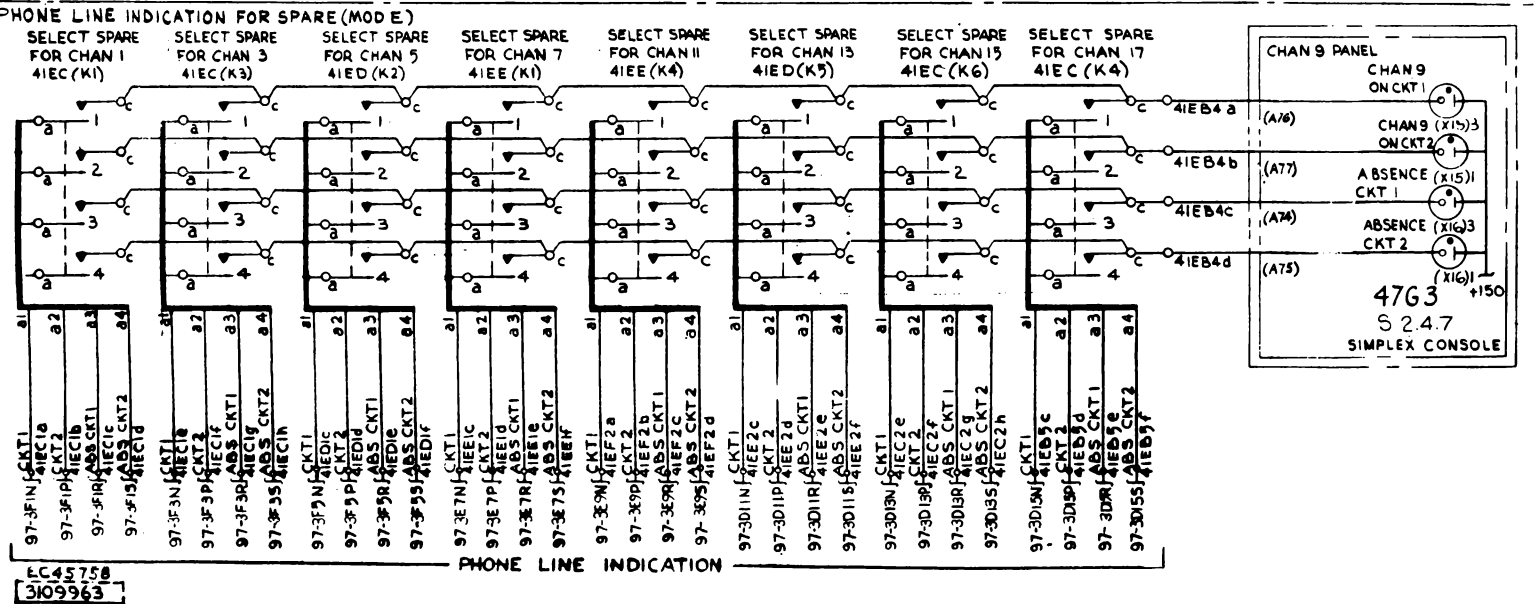
VI THIS DRAWING IS FOR A COMPLETE UNIT 41 WHEN UNWired SHIPPING SECTIONS
J-M AND I OR X-(BB) ARE SUBSTITUTED THE CIRCUITRY
FOR THE REMAINING SHIPPING SECTIONS IS NOT APPLICABLE THE
WIRING FROM THE COMMON EQUIPMENT TO THE UNIT BREAK
REMAINS AS SHOWN IN CHARTS I, II, III & IV (EXAMPLE: LONGWORD
ON B HEADS OF J1 & J2). THESE CONNECTIONS, THAT ARE
NOT USED ARE GROUNDED ON THE UNWired SHIPPING
SECTIONS.

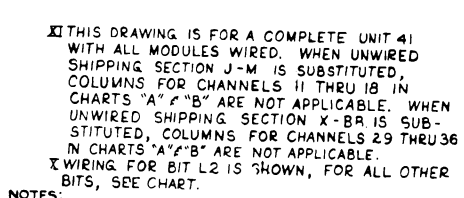
VII THE CONNECTION SHOWN IS FOR MODULE 'A'. FOR OTHER MODULES SEE
CHARTS:
"A" LONGWORD - CHART I
"B" LONGWORD - CHART II
"A" READOUT ALARM - CHART III
"B" READOUT ALARM - CHART IV

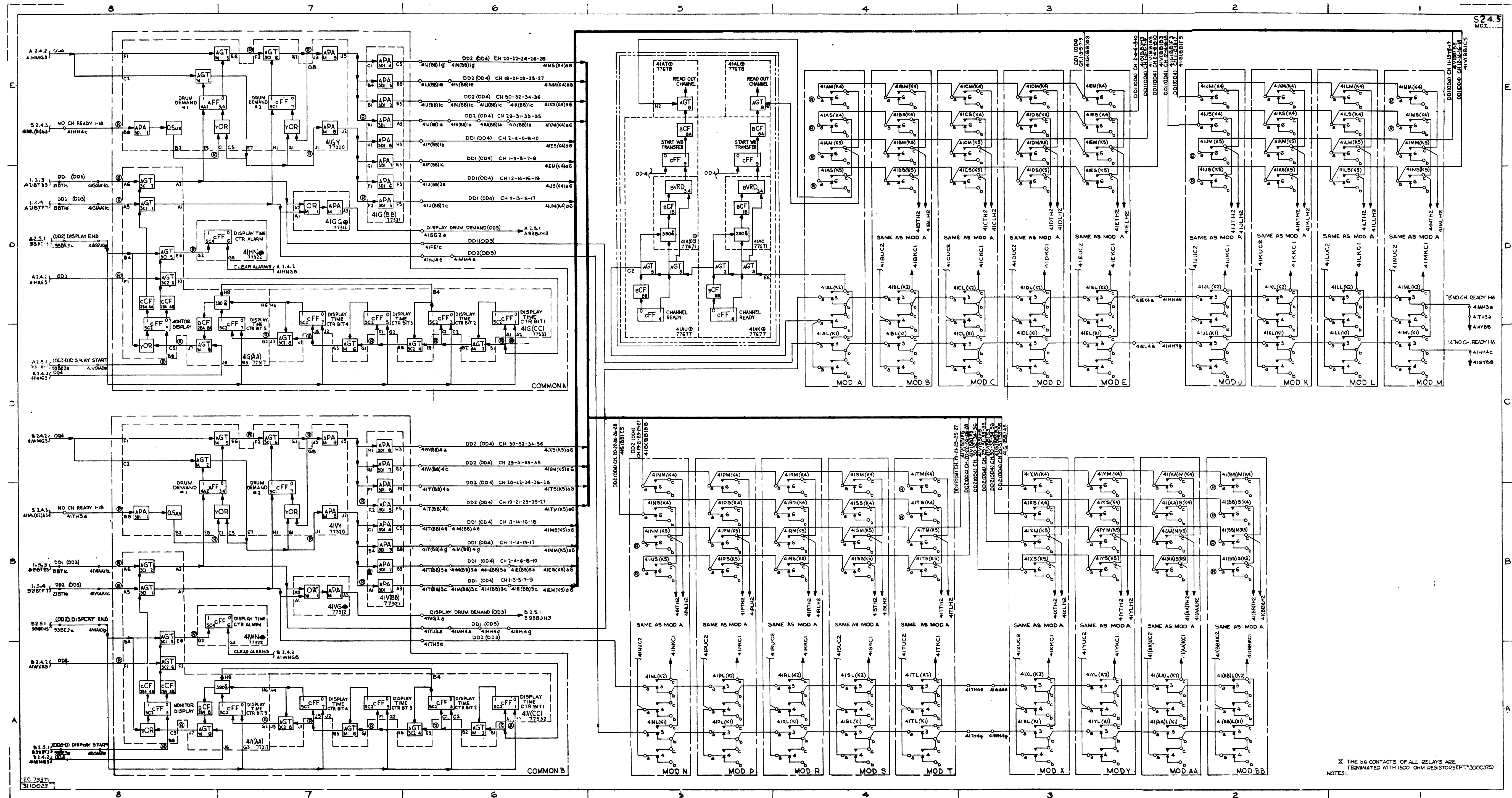
X MODULES A,B,C,D,E,J,K,L,M,N,P,R,S,T,
X,Y,(AA),(BB)



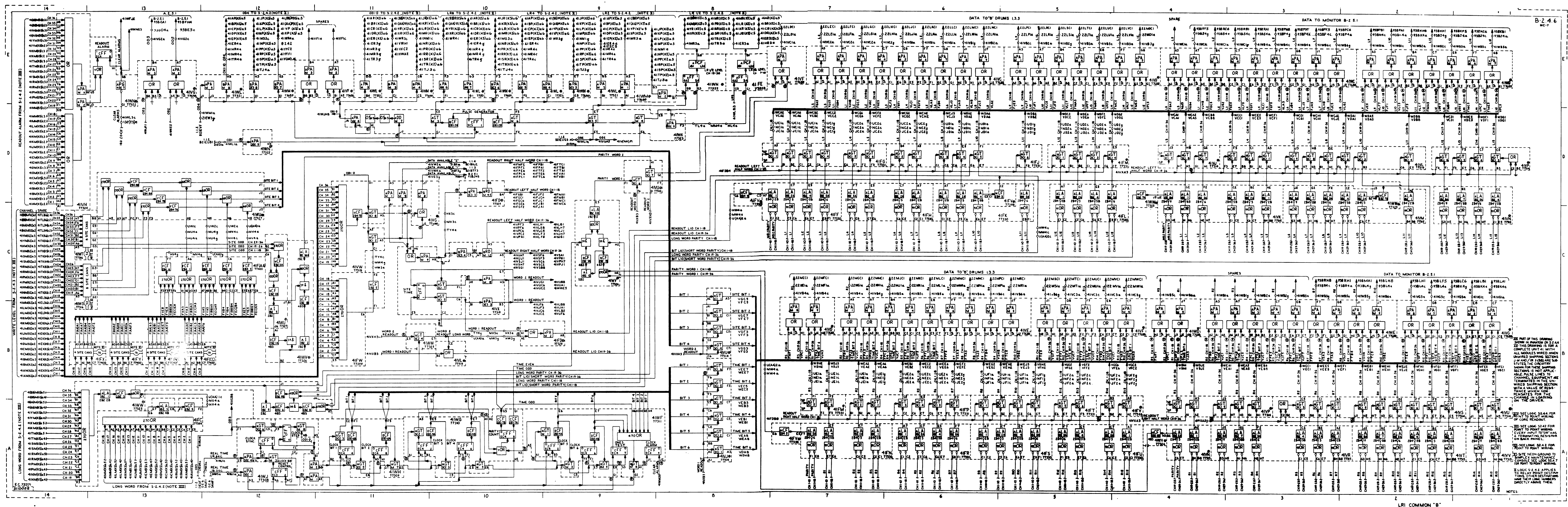
XI THE CHART BELOW SHOWS ALL POSSIBLE SELECTIONS FOR THE "A" OR "B" RELAYS IN ANY CHANNEL MODULE.
 UNIT STATUS SWITCH COMP A ACTIVE COMP B ACTIVE
 SYB516 Y MC 15 RELAYS PICKED 15 RELAYS PICKED
 OFF POWER FIL NO RELAYS PICKED NO RELAYS PICKED
 X THE REPRESENTATION
 41JM(K)2-41JM2 41HTAS 46W6 41EVS 41EKS
 INDICATES INFORMATION FLOW TO 41HTAS AND 41GW6 FROM THE CHANNEL OR THE SPARE CHANNEL DEPENDING ON RELAYS ENERGIZED. PT TO PT. WIRING IS FROM 41JM(K)2 TO 41JM2 TO 41HTAS TO 41GW6 TO 41EVS TO 41EKS.







DRUM DEMAND LRI



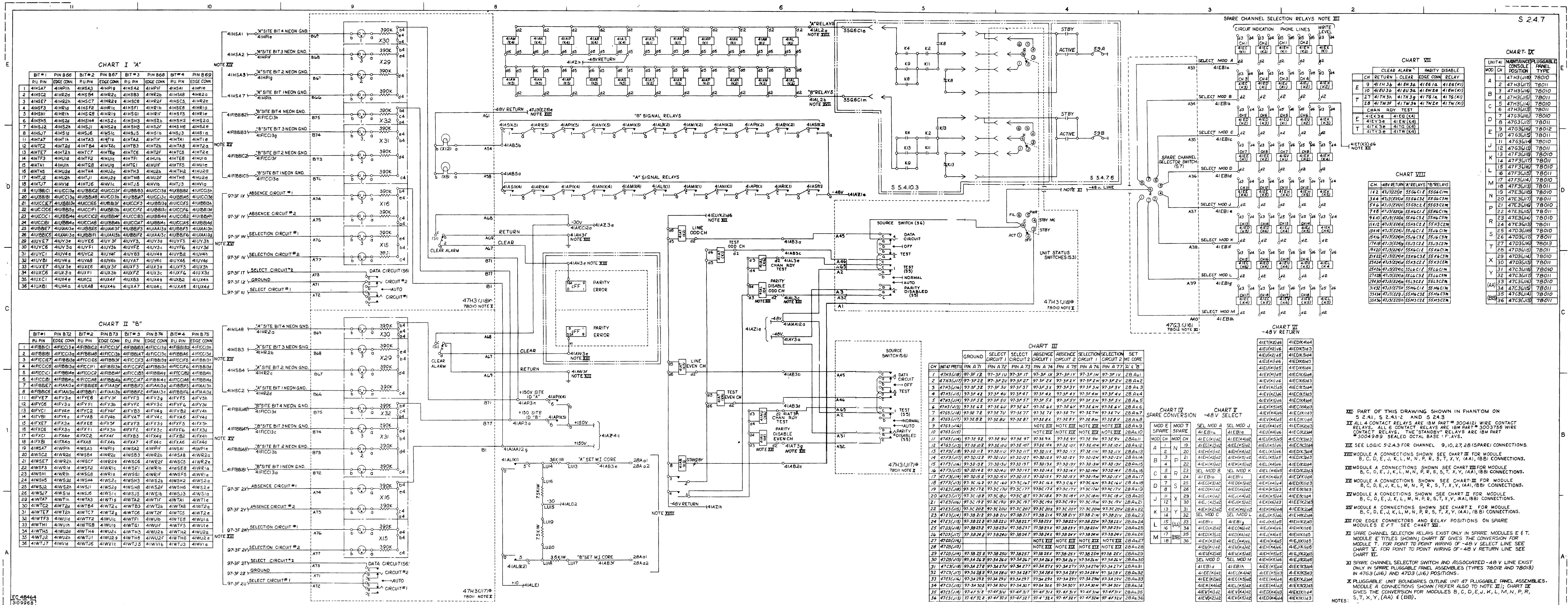
B-2.4.6
MC-7

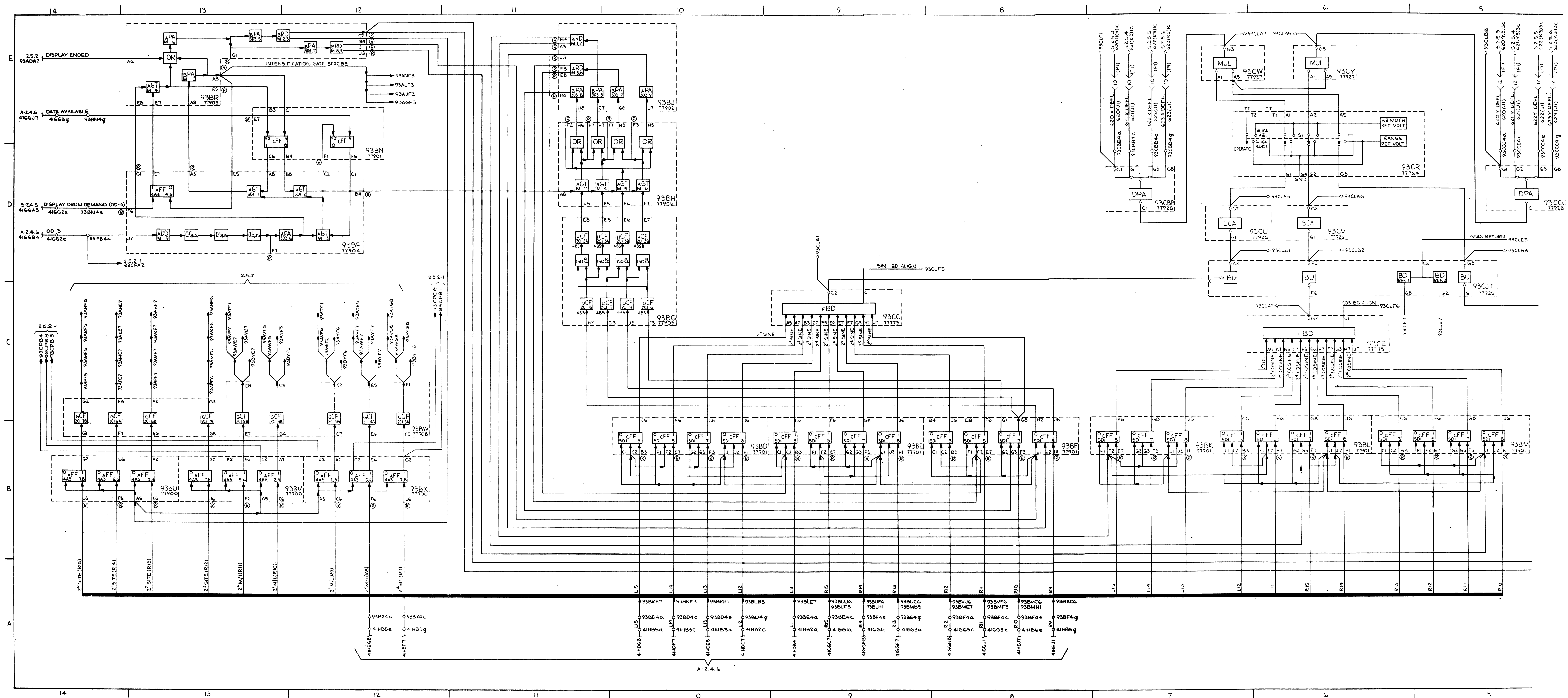
THE PART OF THIS DRAWING
SHOWN IN PARAGRAPH 3.2.4.4
IS THE COMPLETE UNIT. ALL
UNWID SHIPPED SECTIONS
J-4 AND/OR J-5 ARE SHOWN
WITH THE CIPHERY
SHOWN FOR THESE SHIPPED
SECTIONS IS NOT APPLIC-
ABLE. PULSE LINES TO
CHANNEL EQUIPMENT ARE
TERMINATED IN THE UN-
WID SHIPPED SECTION
WITH A VALUE OF RESIS-
TANCE INDICATED FOR THE
CHANNEL EQUIPMENT
ON THE DRIVER.

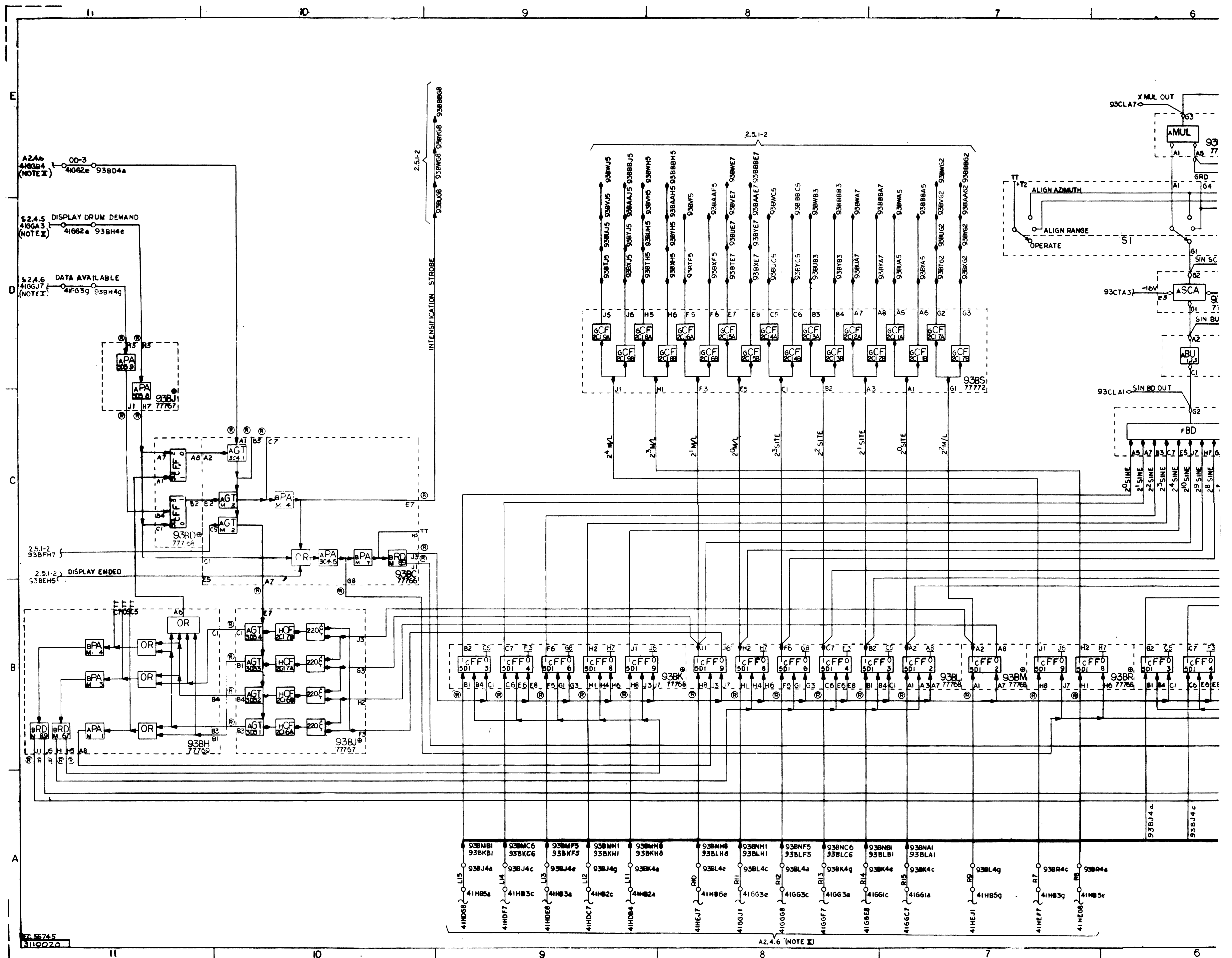
THE SET LOGIC 32.4.4 FOR
POINT TO POINT WIRING
IS NOT APPLICABLE AS
A TERMINATING RESISTOR
IS REQUIRED TO RELAY POINT
TO POINT WIRING.

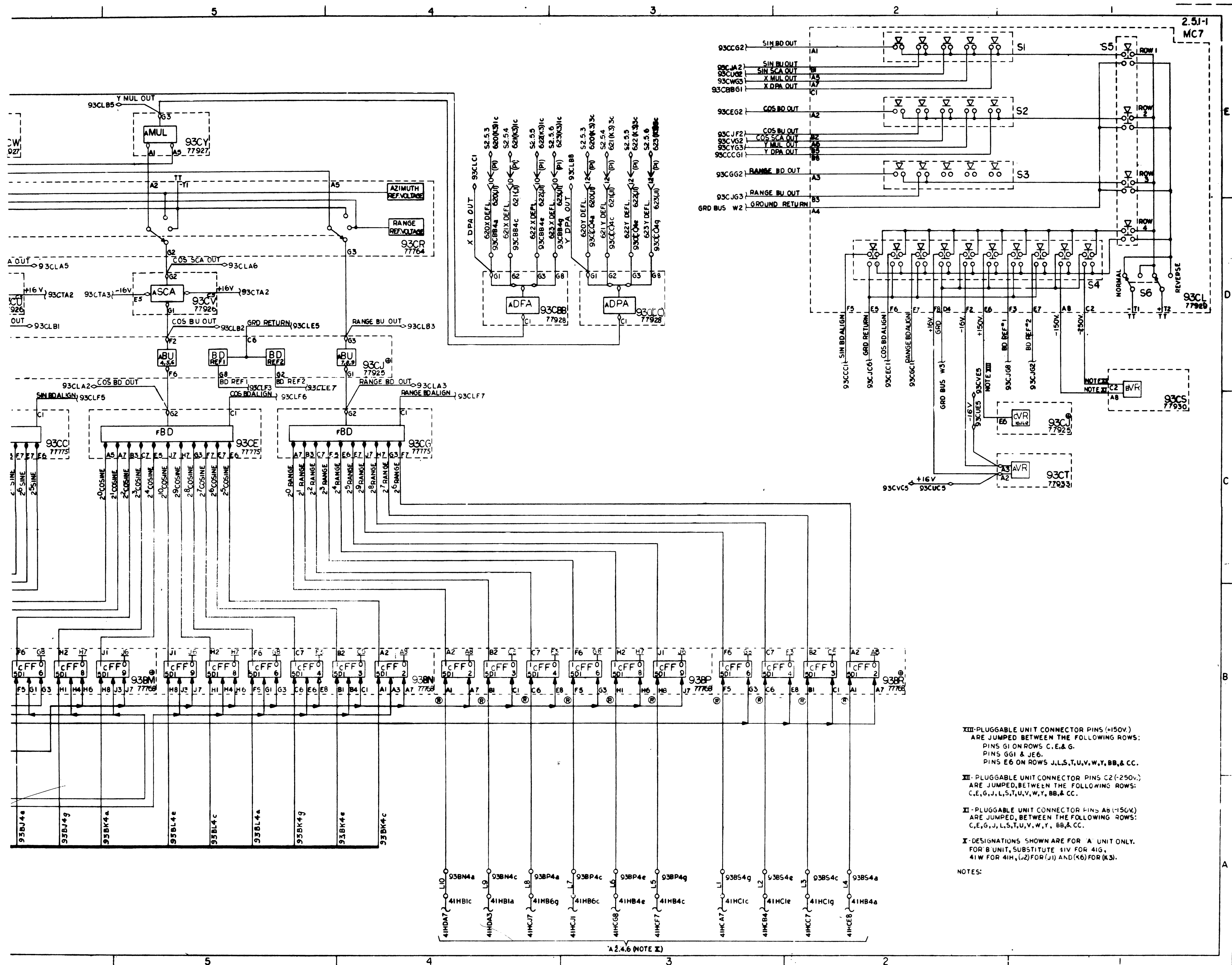
THE SET LOGIC 32.4.4 FOR
POINT TO POINT WIRING
IS NOT APPLICABLE AS
A TERMINATING RESISTOR
IS REQUIRED TO RELAY POINT
TO POINT WIRING.

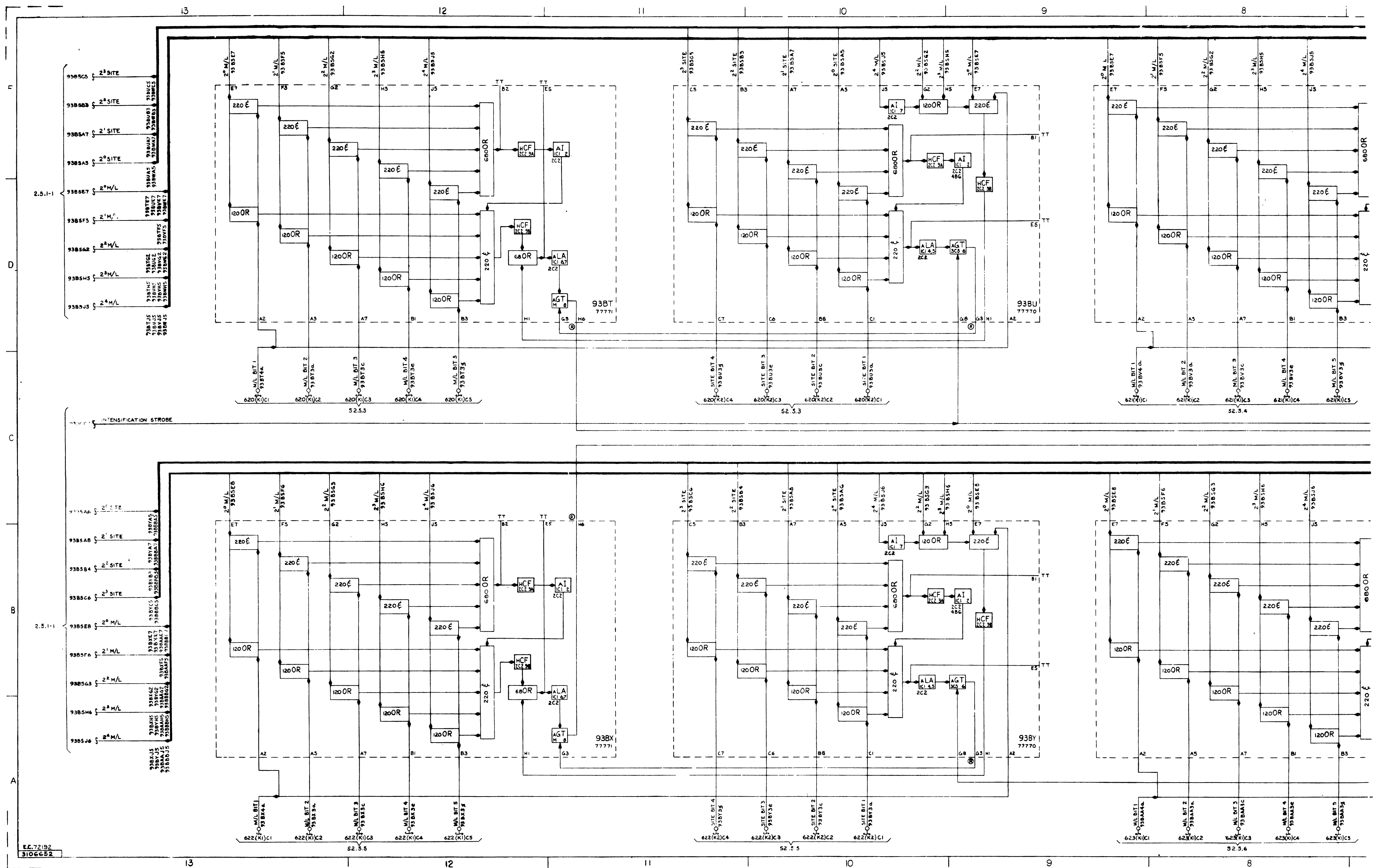
THE SET LOGIC 32.4.4 FOR
POINT TO POINT WIRING
IS NOT APPLICABLE AS
A TERMINATING RESISTOR
IS REQUIRED TO RELAY POINT
TO POINT WIRING.

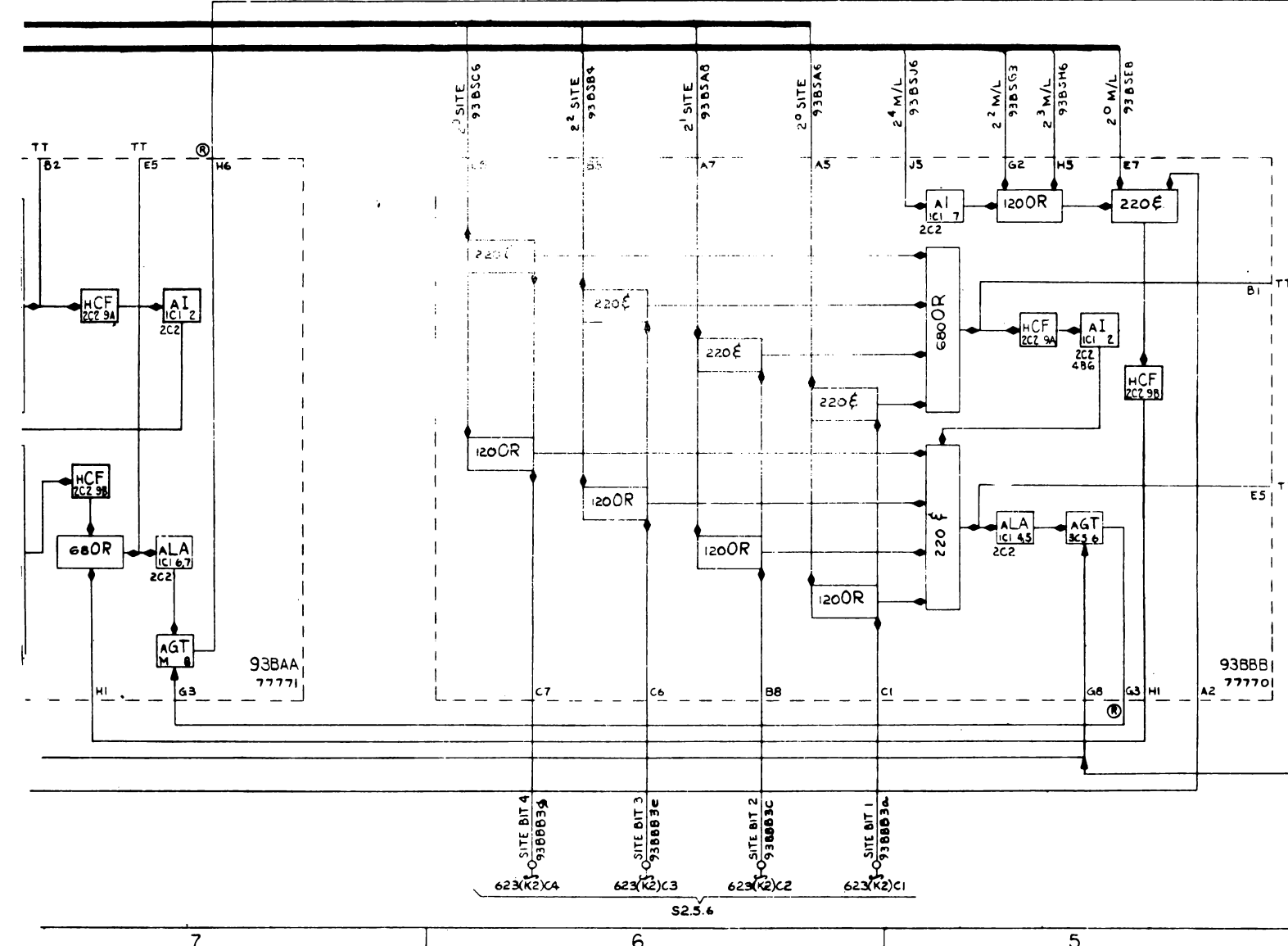


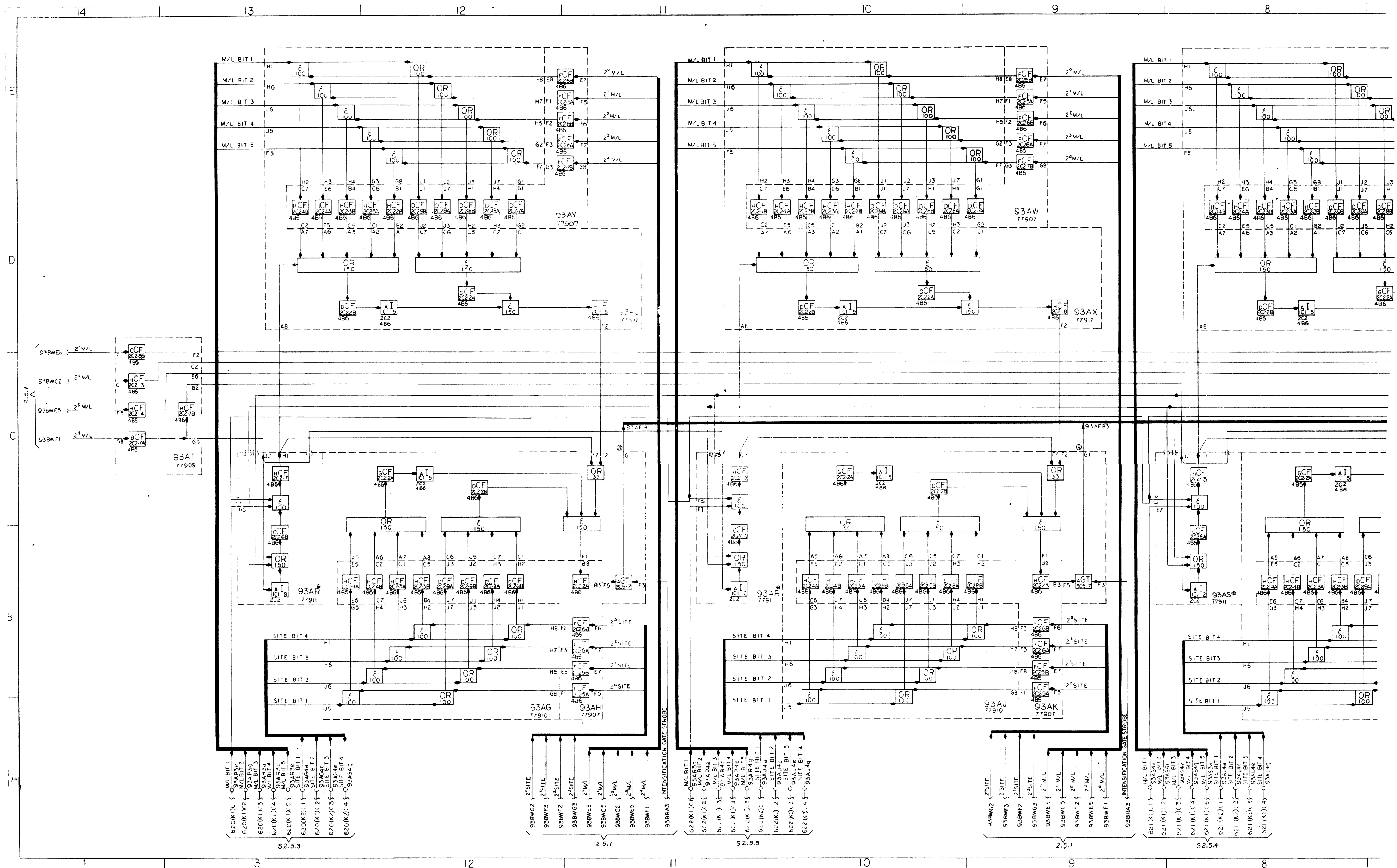


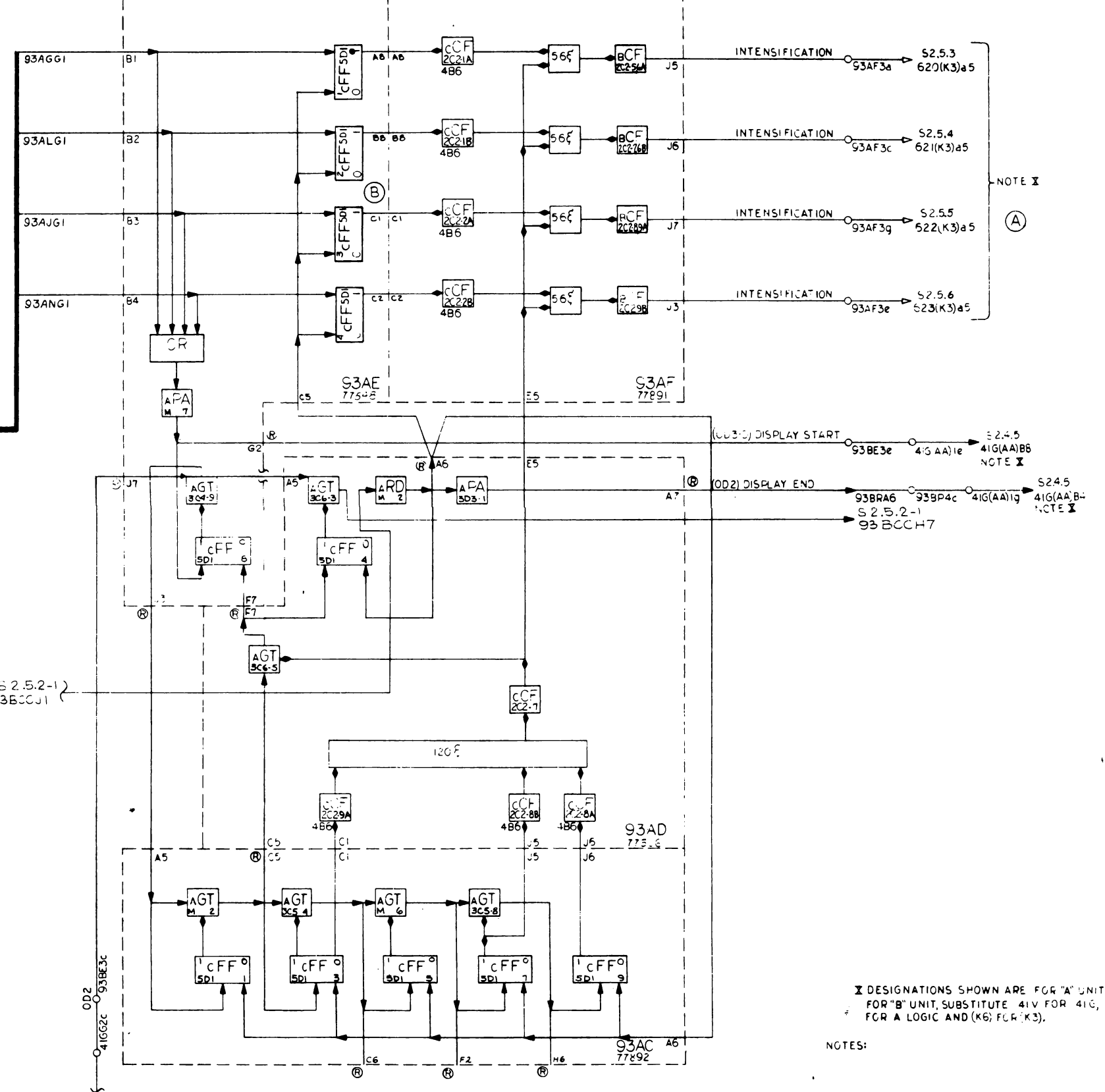
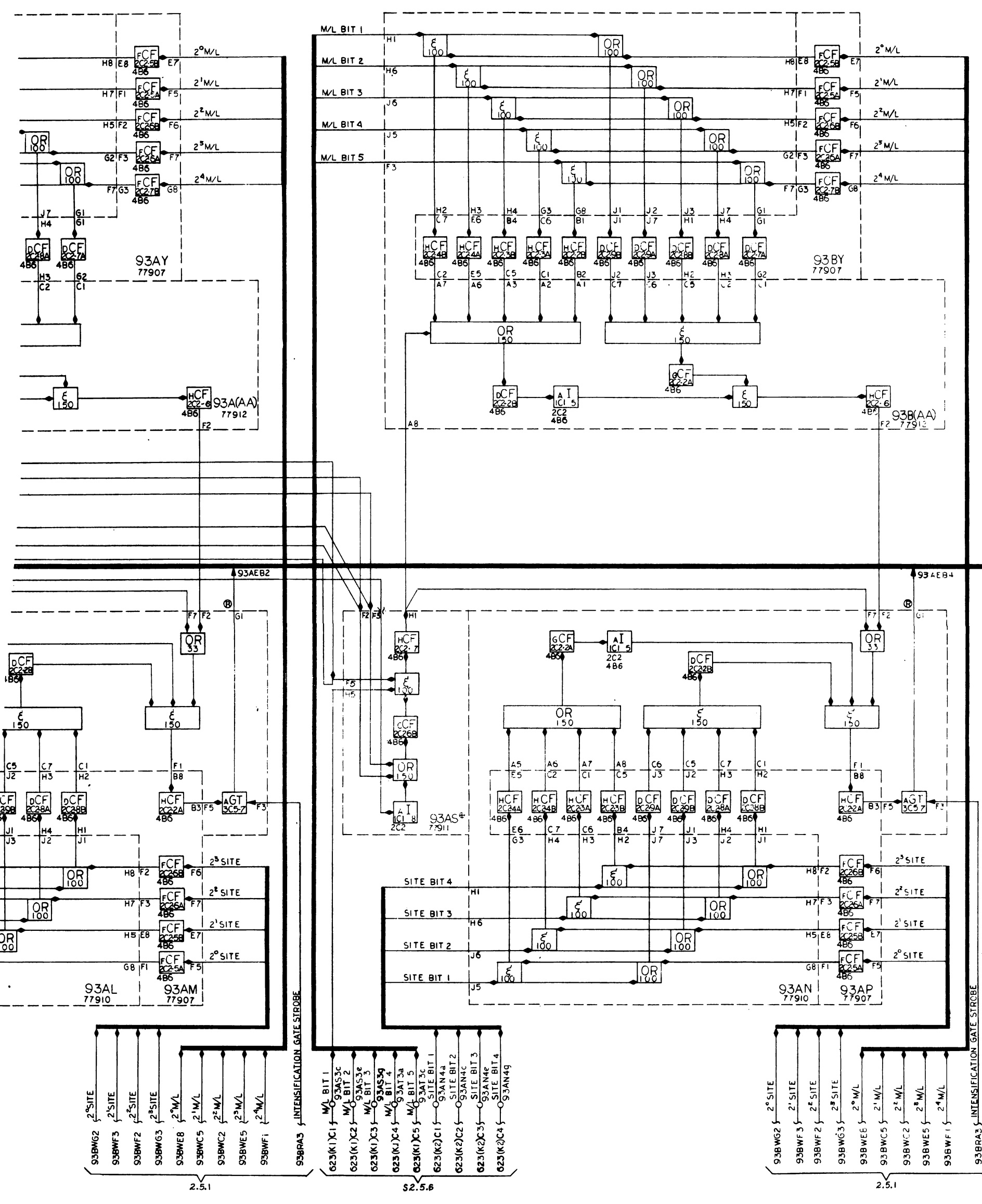






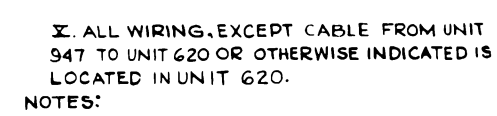


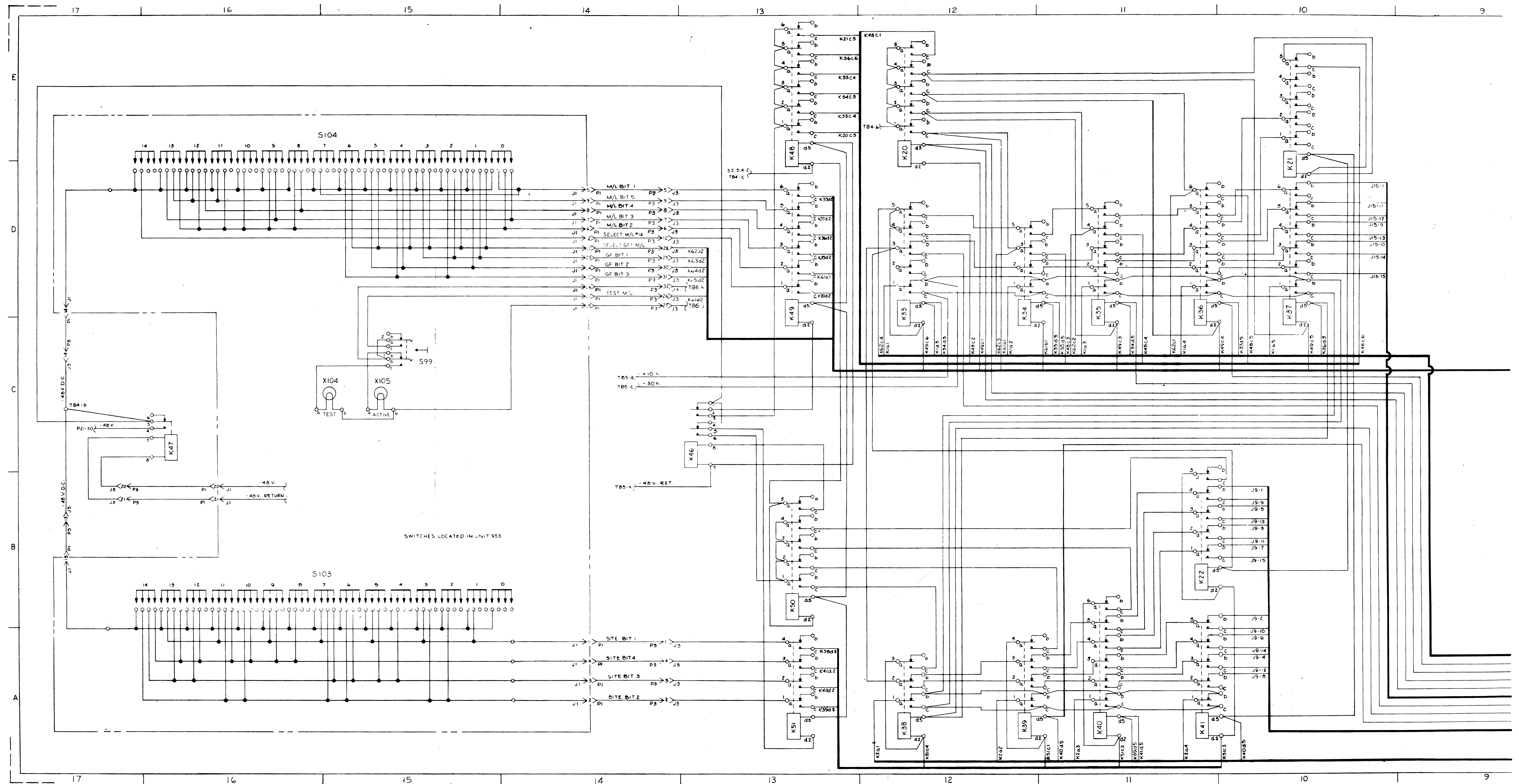


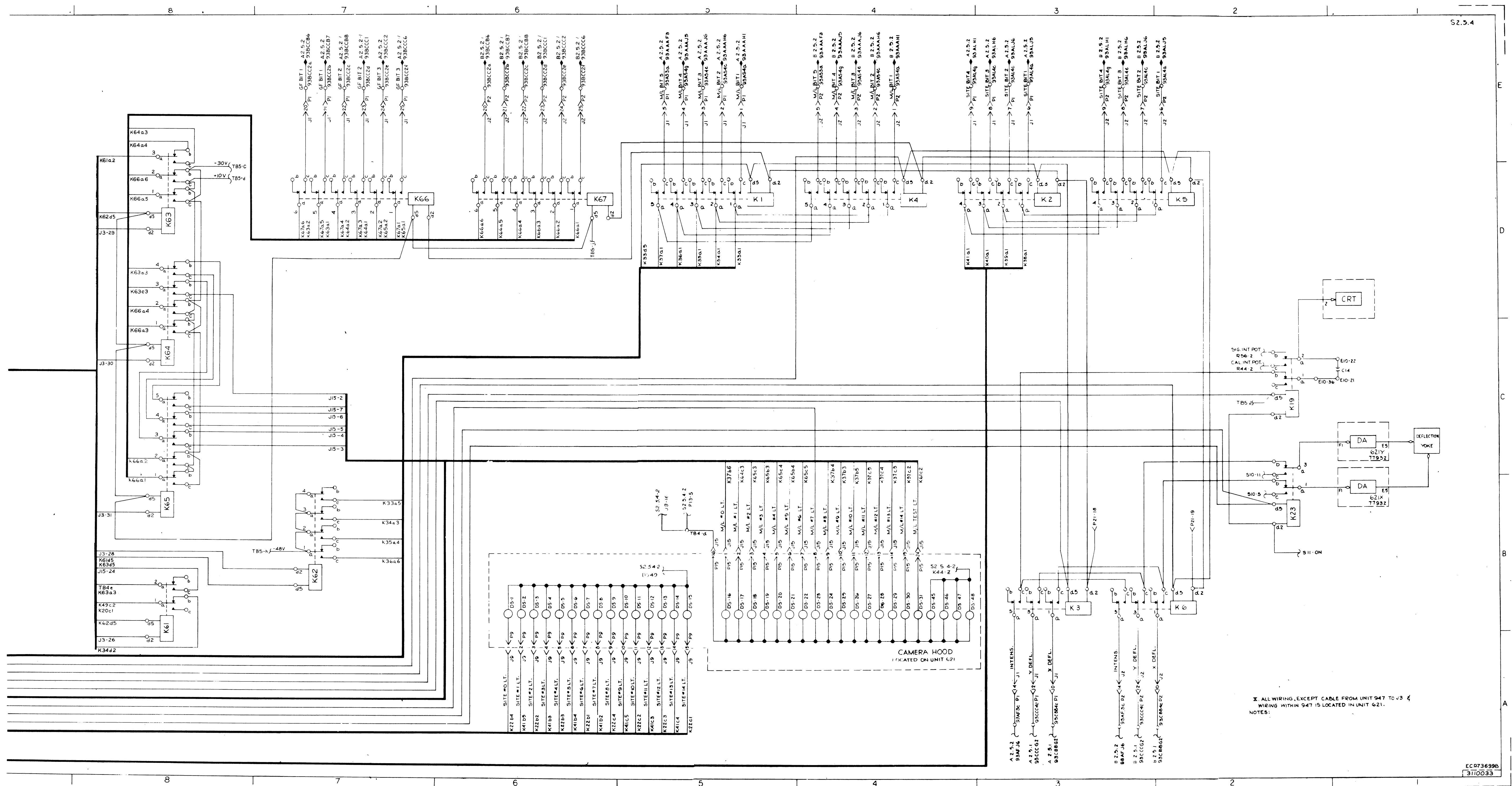


DESIGNATIONS SHOWN ARE FOR "A" UNIT ONLY.
FOR "B" UNIT, SUBSTITUTE 41V FOR 41G, B LOGIC
FOR A LOGIC AND (K) FOR (K3).

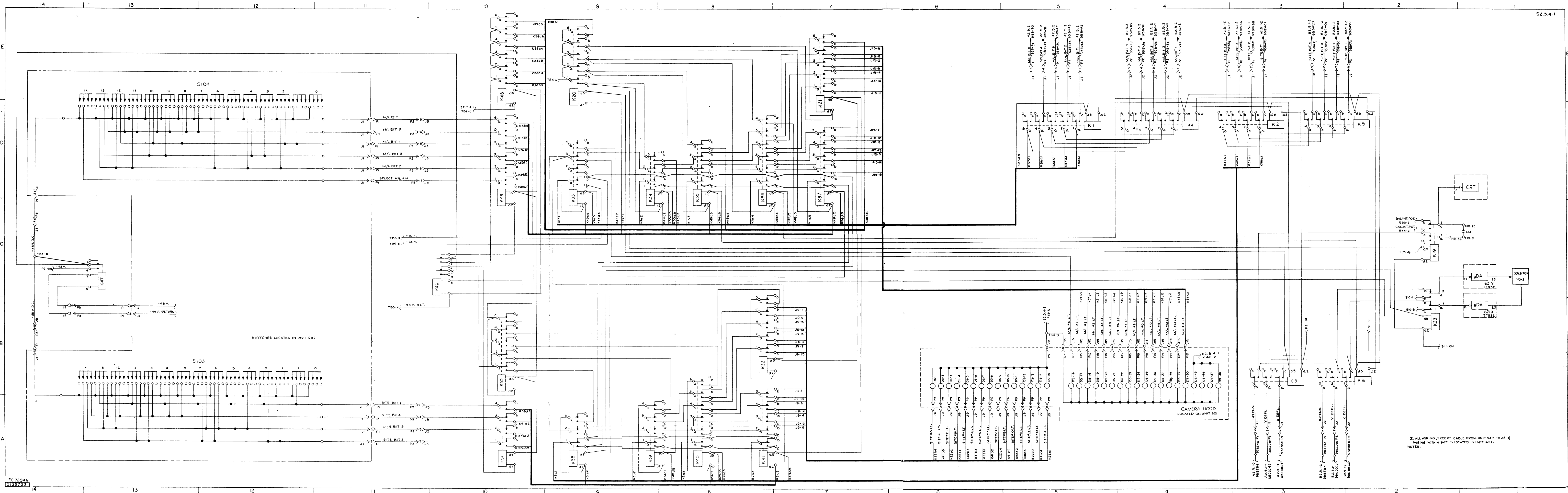
NOTES:





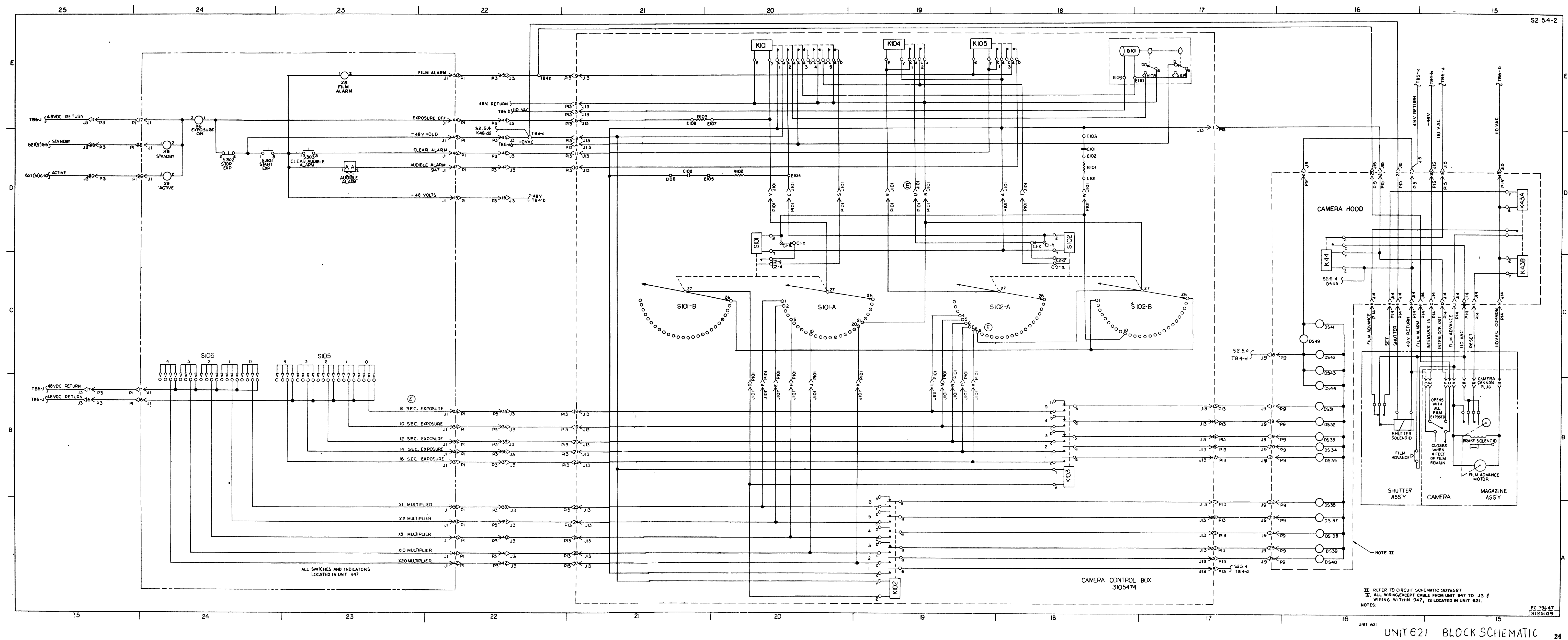


BLOCK SCHEMATIC UNIT 621

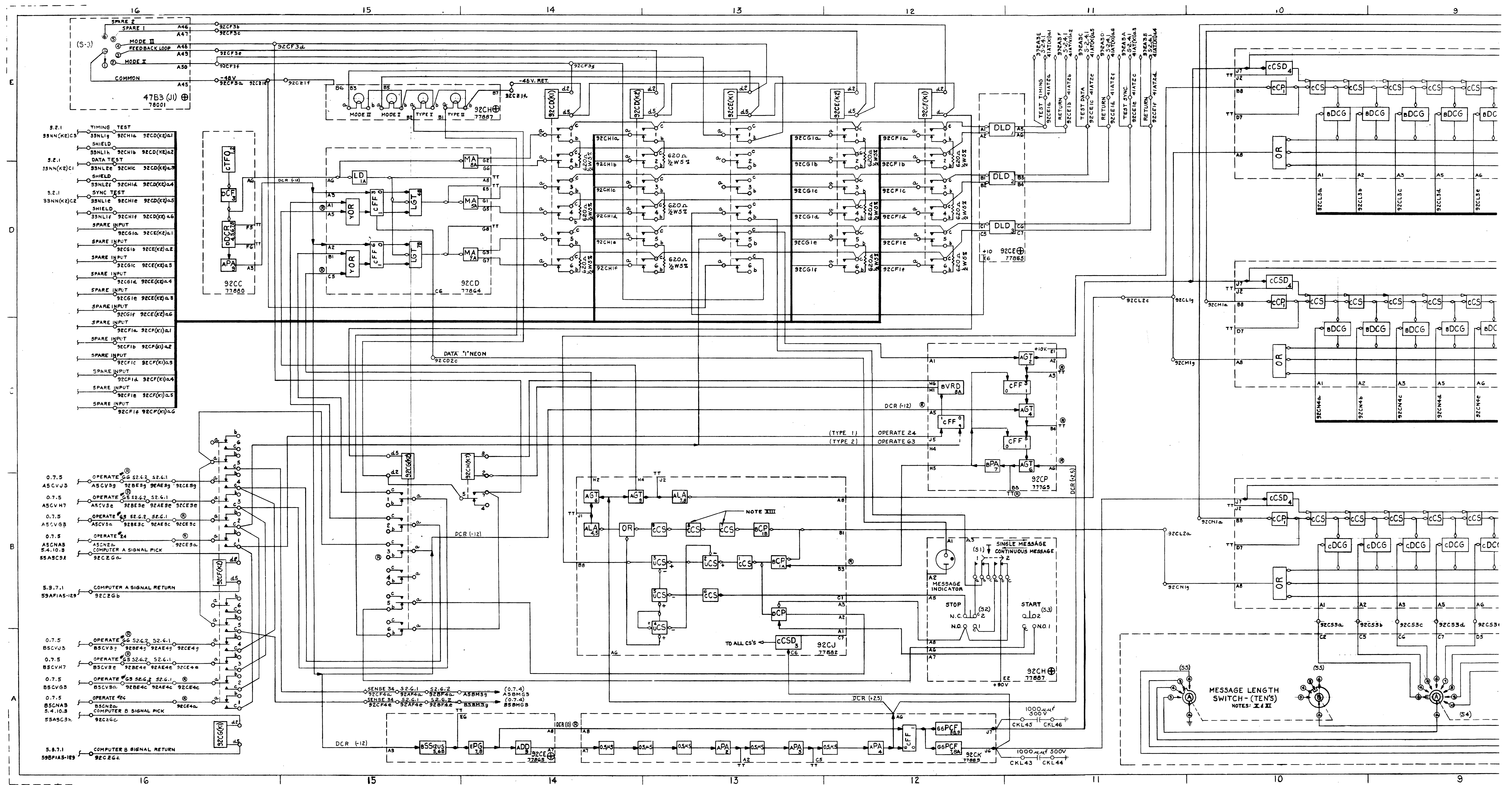


EC 72546
3138763

BLOCK SCHEMATIC UNIT 621

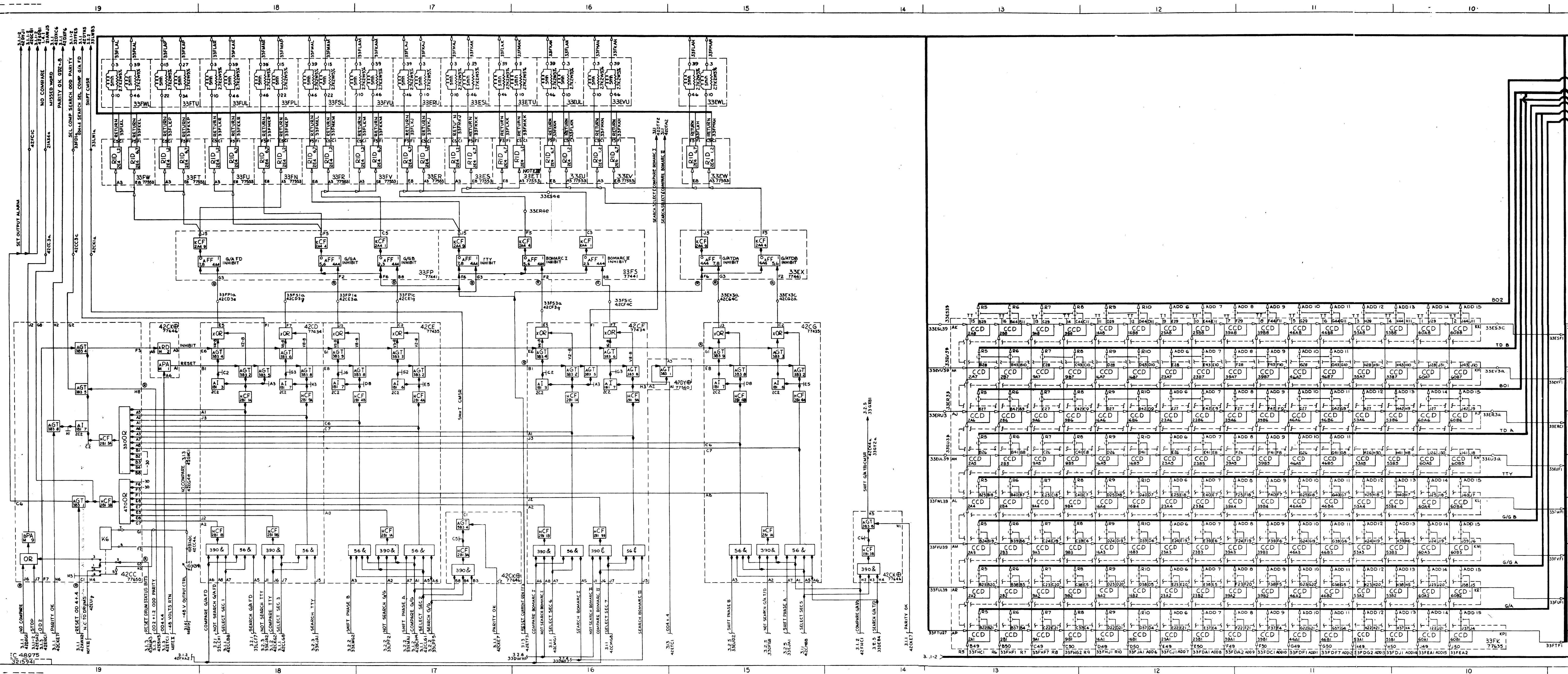


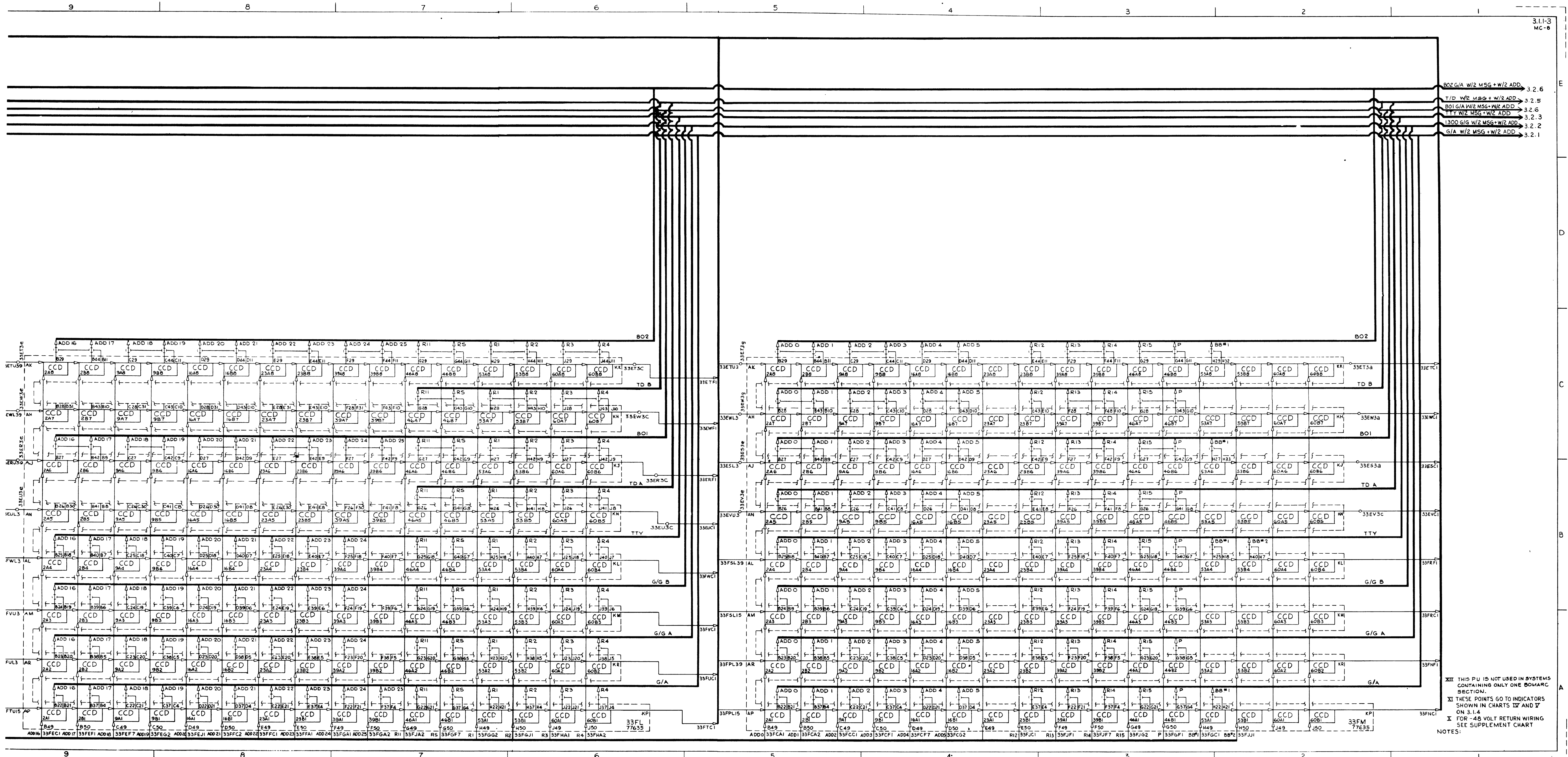












NOTES:
XIII THIS PU IS NOT USED IN SYSTEMS CONTAINING ONLY ONE BOMARC SECTION.
XI THESE POINTS GO TO INDICATORS SHOWN IN CHARTS IX AND V ON 3.1.4
X FOR -48 VOLT RETURN WIRING SEE SUPPLEMENT CHART



